

N & P-Channel 40-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

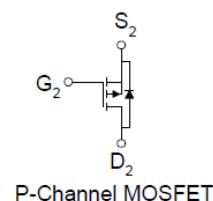
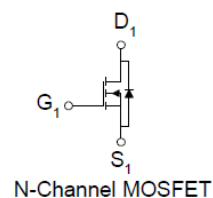
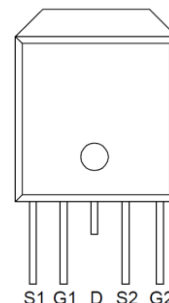
Typical Applications:

- Automotive Systems
- DC/DC Conversion Circuits
- Battery Powered Power Tools

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
40	36 @ $V_{GS} = 10V$	30
	46 @ $V_{GS} = 4.5V$	27
-40	38 @ $V_{GS} = -10V$	-30
	48 @ $V_{GS} = -4.5V$	-26



RoHS
COMPLIANT
HALOGEN
FREE



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Nch Limit	Pch Limit	Units
Drain-Source Voltage	V_{DS}	40	-40	V
Gate-Source Voltage	V_{GS}	± 20	± 20	
Continuous Drain Current ^a	I_D	30	-30	A
Pulsed Drain Current ^b	I_{DM}	100	-100	
Continuous Source Current (Diode Conduction) ^a	I_S	15	-15	A
Power Dissipation ^a	P_D	50	50	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^c	$R_{\theta JA}$	50	$^\circ\text{C/W}$
Maximum Junction-to-Case	$R_{\theta JC}$	3	

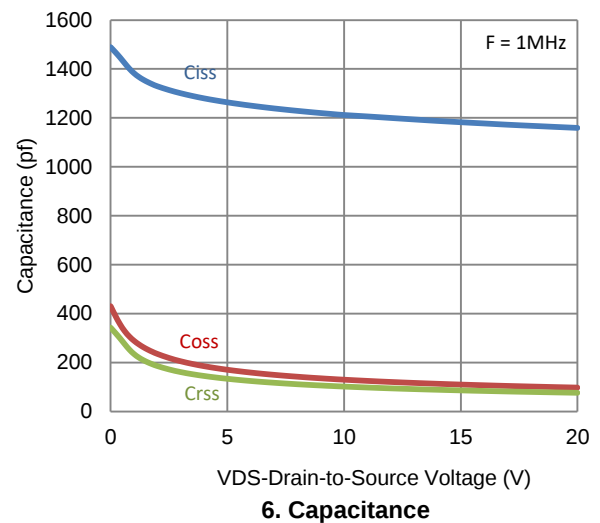
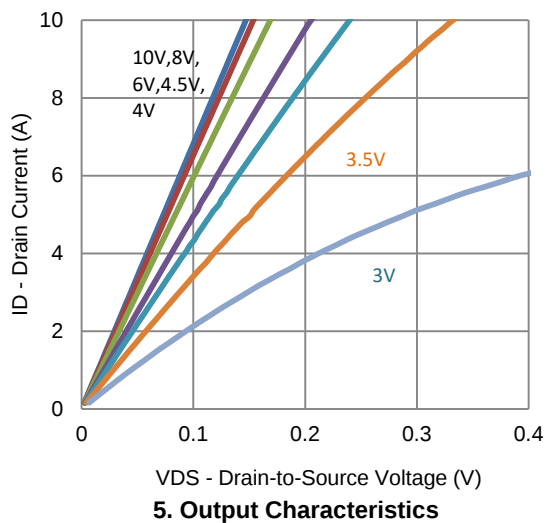
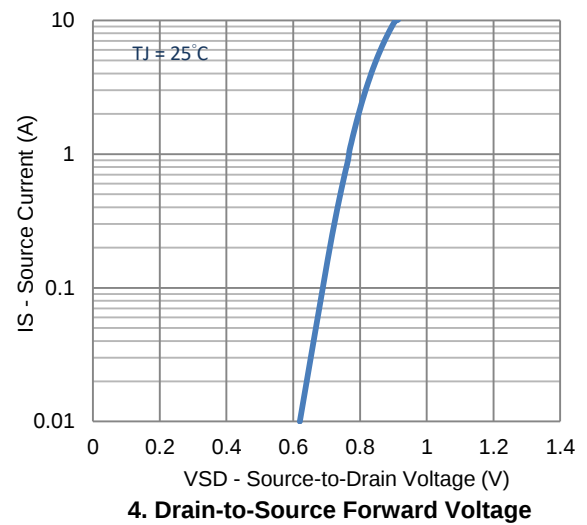
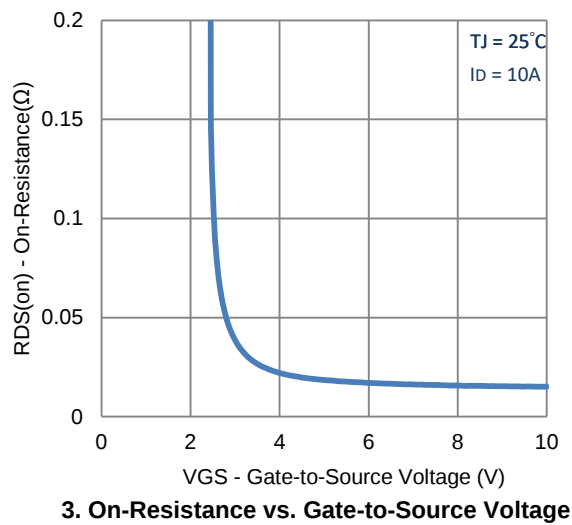
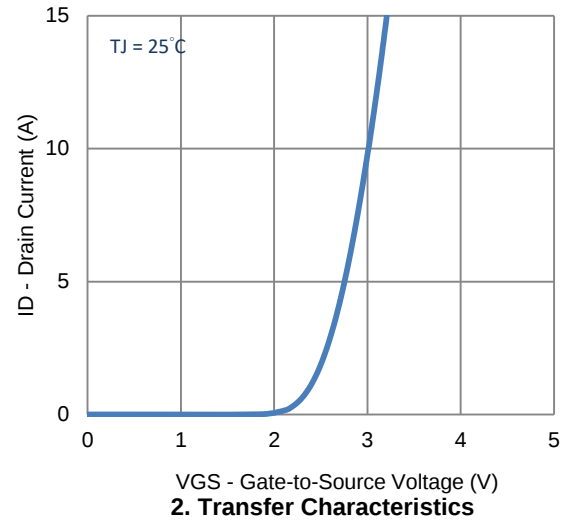
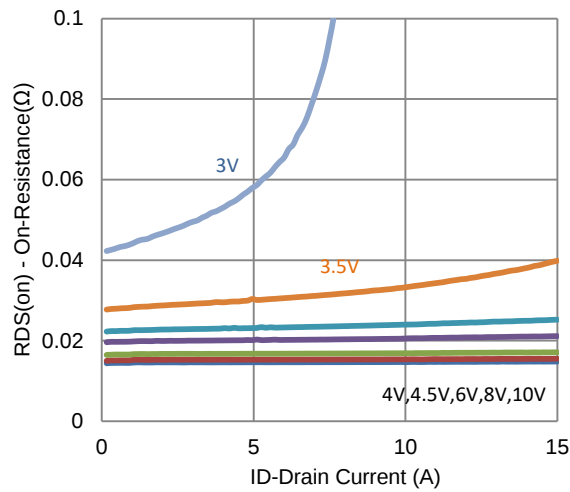
Notes

- Package Limited
- Pulse width limited by maximum junction temperature
- Surface Mounted on 1" x 1" FR4 Board.

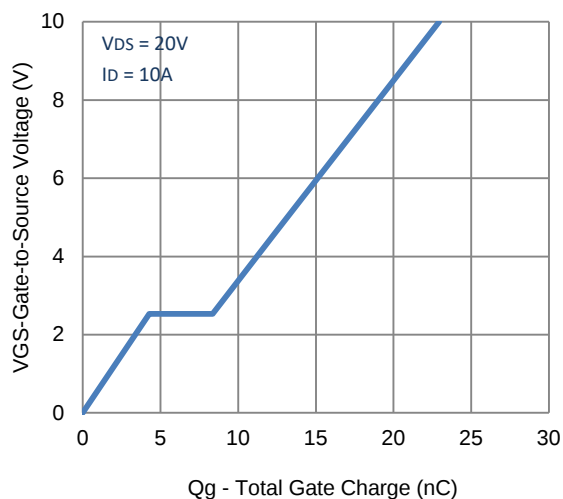
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 uA (Nch)	1			V
		V _{DS} = V _{GS} , I _D = -250 uA (Pch)	-1			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 32 V, V _{GS} = 0 V (Nch)			1	uA
		V _{DS} = -32 V, V _{GS} = 0 V (Pch)			-1	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V (Nch)	10			A
		V _{DS} = -5 V, V _{GS} = -10 V (Pch)	-10			A
Drain-Source On-Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A (Nch)			36	mΩ
		V _{GS} = 4.5 V, I _D = 13 A (Nch)			46	
		V _{GS} = -10 V, I _D = -10 A (Pch)			38	mΩ
		V _{GS} = -4.5 V, I _D = -8 A (Pch)			48	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 15 A (Nch)		27		S
		V _{DS} = -15 V, I _D = -14 A (Pch)		18		S
Diode Forward Voltage ^a	V _{SD}	I _S = 7.5 A, V _{GS} = 0 V (Nch)		0.88		V
		I _S = -7.5 A, V _{GS} = 0 V (Pch)		-0.93		V
Dynamic ^b						
Total Gate Charge	Q _g	N - Channel V _{DS} = 20 V, V _{GS} = 4.5 V, I _D = 10 A		12		nC
Gate-Source Charge	Q _{gs}			4.3		
Gate-Drain Charge	Q _{gd}			4.1		
Turn-On Delay Time	t _{d(on)}	N - Channel V _{DS} = 20 V, R _L = 2 Ω, I _D = 10 A, V _{GEN} = 10 V, R _{GEN} = 6 Ω		5		ns
Rise Time	t _r			7		
Turn-Off Delay Time	t _{d(off)}			35		
Fall Time	t _f			11		
Input Capacitance	C _{iss}	N - Channel V _{DS} = 15 V, V _{GS} = 0 V, f = 1 Mhz		1182		pF
Output Capacitance	C _{oss}			110		
Reverse Transfer Capacitance	C _{rss}			86		
Total Gate Charge	Q _g	P - Channel V _{DS} = -20 V, V _{GS} = -4.5 V, I _D = -10 A		15		nC
Gate-Source Charge	Q _{gs}			4.2		
Gate-Drain Charge	Q _{gd}			4.6		
Turn-On Delay Time	t _{d(on)}	P - Channel V _{DS} = -20 V, R _L = 2 Ω, I _D = -10 A, V _{GEN} = -10 V, R _{GEN} = 6 Ω		7		ns
Rise Time	t _r			6		
Turn-Off Delay Time	t _{d(off)}			52		
Fall Time	t _f			19		
Input Capacitance	C _{iss}	P - Channel V _{DS} = -15 V, V _{GS} = 0 V, f = 1 Mhz		1264		pF
Output Capacitance	C _{oss}			110		
Reverse Transfer Capacitance	C _{rss}			84		

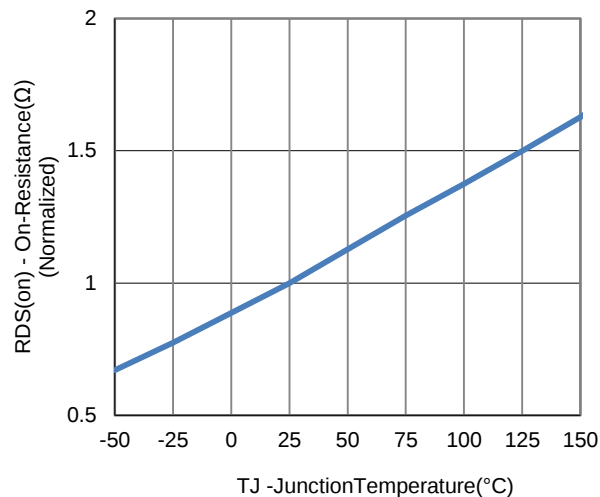
Typical Electrical Characteristics - N-channel



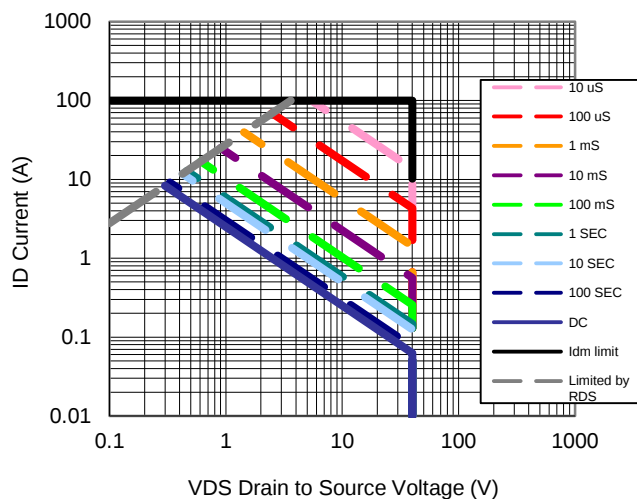
Typical Electrical Characteristics - N-channel



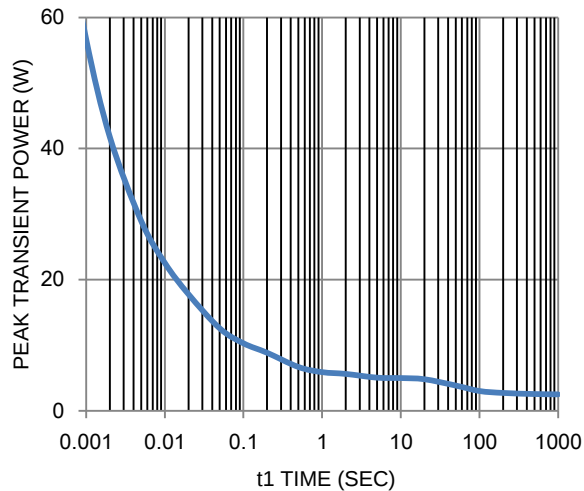
7. Gate Charge



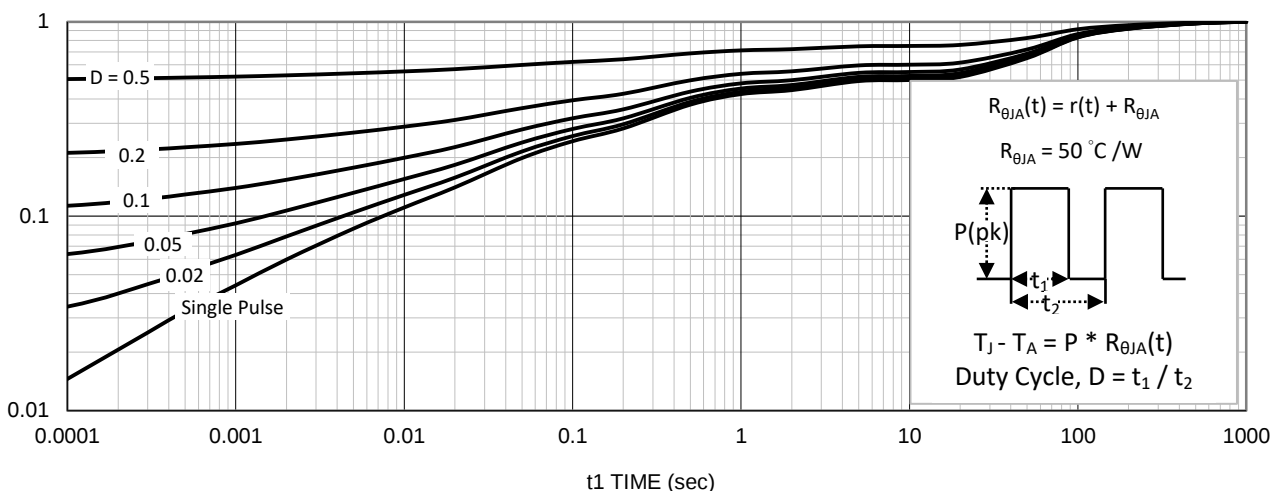
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

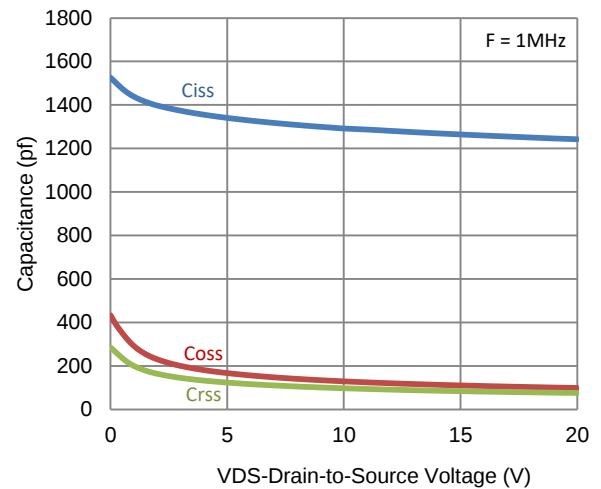
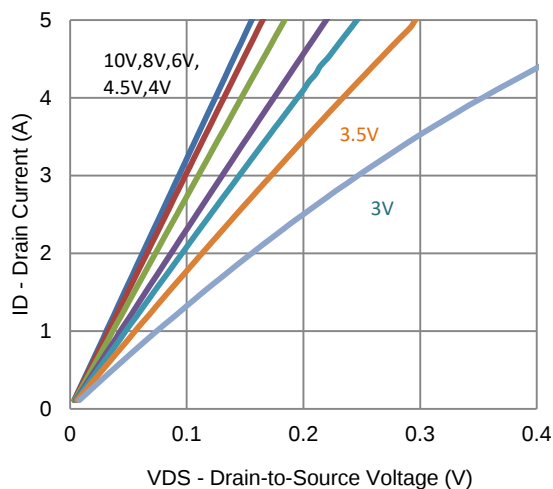
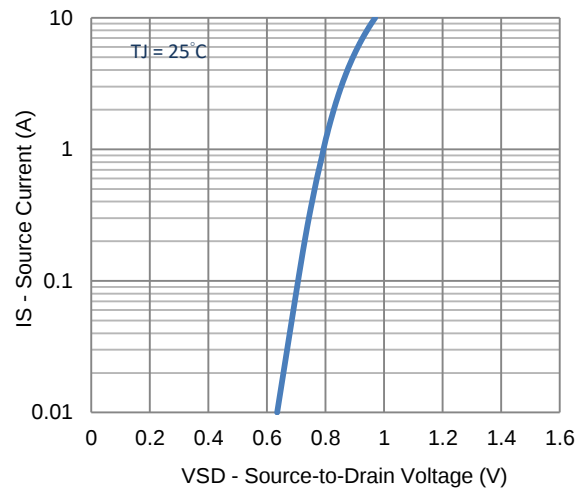
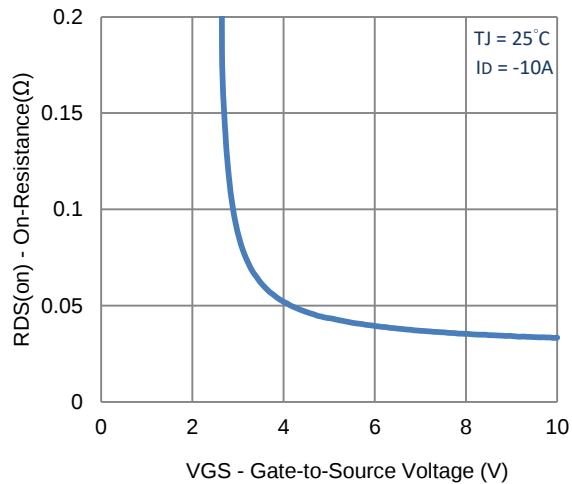
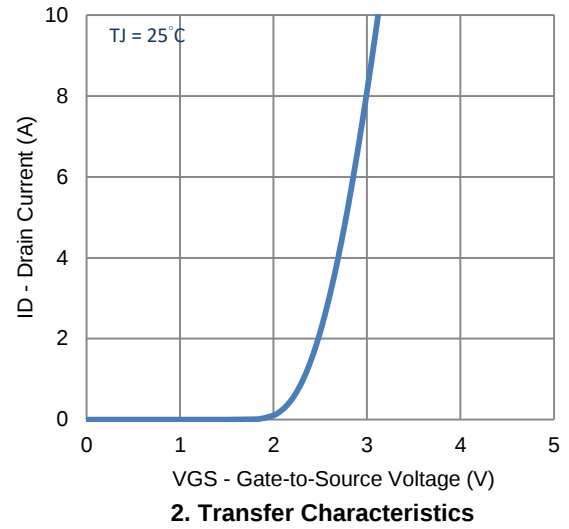
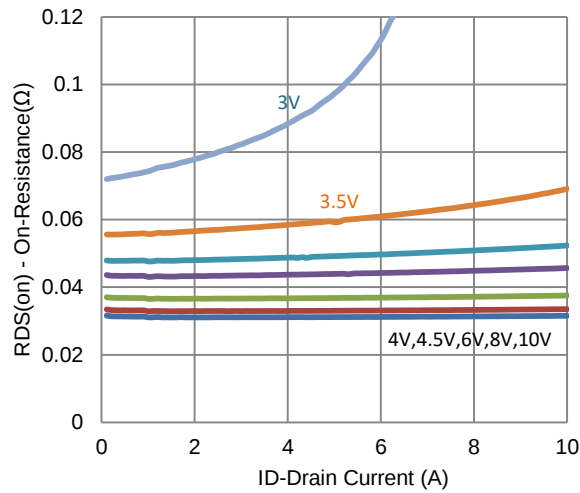


10. Single Pulse Maximum Power Dissipation

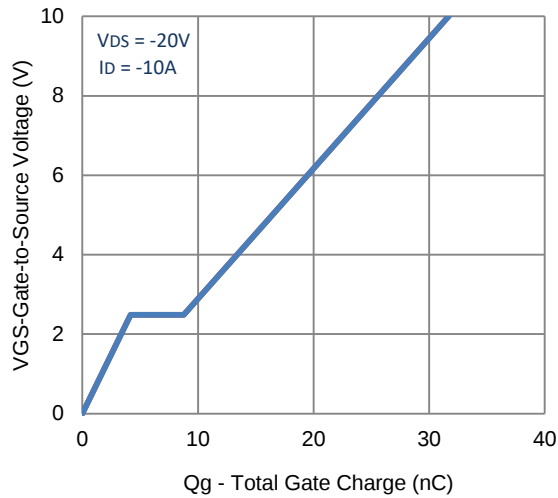


11. Normalized Thermal Transient Junction to Ambient

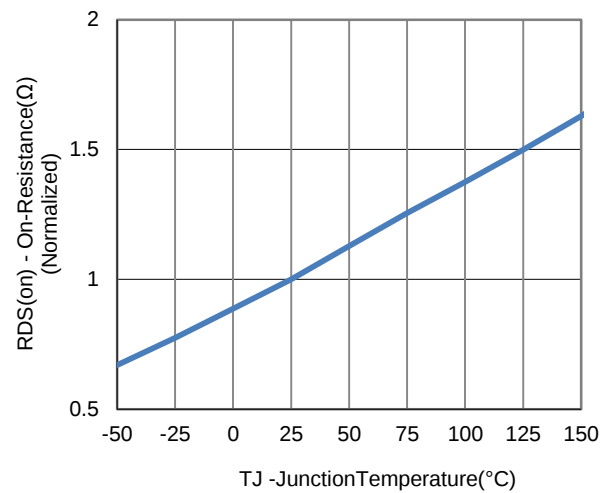
Typical Electrical Characteristics - P-channel



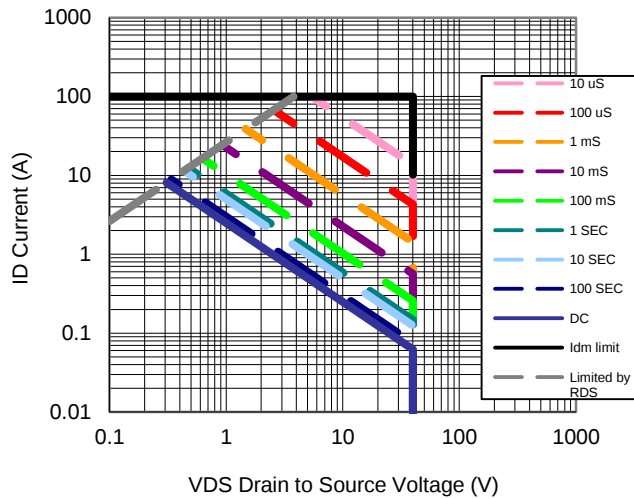
Typical Electrical Characteristics - P-channel



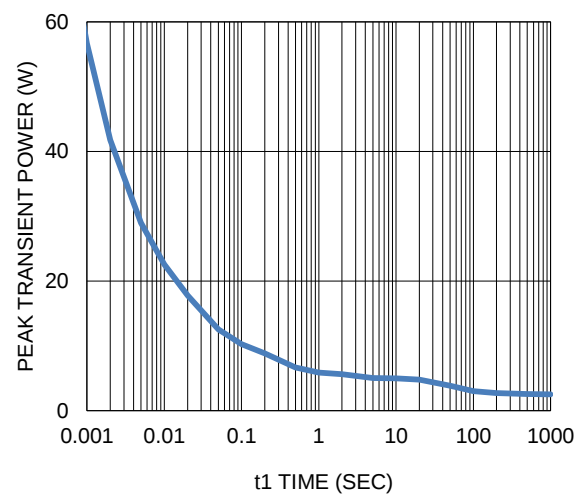
7. Gate Charge



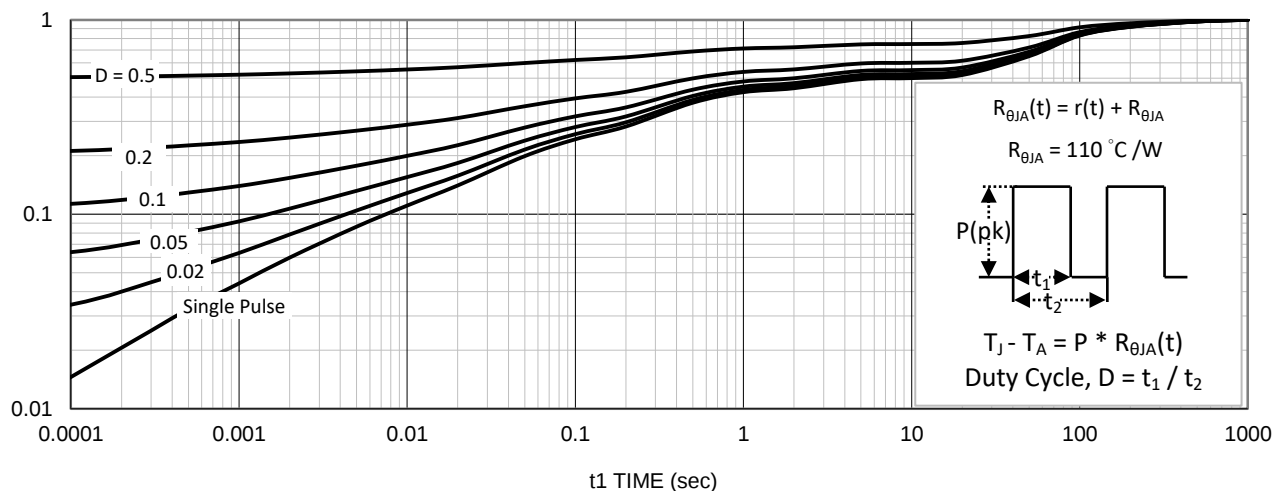
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area



10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information

