### N-Channel 30-V (D-S) MOSFET

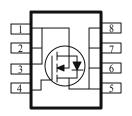
These miniature surface mount MOSFETs utilize High Cell Density process. Low r<sub>DS(on)</sub> assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are PWMDC-DC converters, power management in portable and battery-powered products such as computers, printers, battery charger, telecommunication power system, and telephones power system.

•	Low r <sub>DS(on)</sub> Provides Higher Efficiency and
	Extends Battery Life

- Miniature SO-8 Surface Mount Package Saves Board Space
- High power and current handling capability
- Low side high current DC-DC Converter applications

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$r_{DS(on)} m(\Omega)$	$I_{D}(A)$		
30	$22 @ V_{GS} = 10V$	9.0		
30	$36 @ V_{GS} = 4.5V$	7.0		





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage			30	V	
Gate-Source Voltage			±20	\ \ \	
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25°C		9.0		
Continuous Drain Current	$T_A = 25^{\circ} C$ $T_A = 70^{\circ} C$	$\mathbf{I}_{\mathrm{D}}$	7.4	A	
Pulsed Drain Current <sup>b</sup>			30		
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	1.6	A	
Decree Discipation <sup>8</sup>	T <sub>A</sub> =25°C	D	3.1	W	
Power Dissipation <sup>a</sup>	$T_A = 25^{\circ} C$ $T_A = 70^{\circ} C$	T D	2.0		
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
M · I · · · a	t <= 10 sec	D	40	°C/W		
Maximum Junction-to-Ambient <sup>a</sup>	Steady-State	$R_{ heta JA}$	70	°C/W		

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#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Donomotou	Symbol Test Conditions		Limits			Unit
Parameter			Min	Тур	Max	Umt
Static						
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
Zero Gate Voltage Drain Current	1088	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	
On-State Drain Current <sup>A</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			A
Drain-Source On-Resistance <sup>A</sup>		$V_{GS} = 10 \text{ V}, I_D = 9 \text{ A}$			22	mΩ
Drain-Source On-Resistance	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 7 \text{ A}$			36	
Forward Tranconductance <sup>A</sup>	$\mathbf{g}_{\mathrm{fs}}$	$V_{DS} = 15 \text{ V}, I_{D} = 9 \text{ A}$		40		S
Diode Forward Voltage	$V_{\mathrm{SD}}$	$I_S = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.7		V
Dynamic <sup>b</sup>						
Total Gate Charge	$Q_g$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 9 \text{ A}$		4.0		
Gate-Source Charge	$Q_{gs}$			1.1		nC
Gate-Drain Charge	$Q_{gd}$			1.4		
Turn-On Delay Time	$t_{d(on)}$			16		
Rise Time	$t_{\rm r}$	$V_{DD} = 25 \text{ V}, R_L = 25 \Omega, I_D = 1 \text{ A},$		5		
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}$		23		nS
Fall-Time	$t_{\rm f}$			3		
Source-Ddrain Reverse Recovery Ti	ı t <sub>rr</sub>	$I_F = 2.3 \text{ A, Di/Dt} = 100 \text{ A/uS}$		41		

#### Notes

a. Pulse test:  $PW \le 300us duty cycle \le 2\%$ .

b. Guaranteed by design, not subject to production testing.

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## Typical Electrical Characteristics (N-Channel)

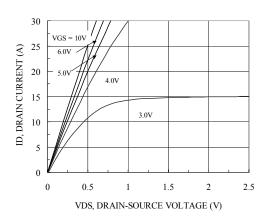


Figure 1. On-Region Characteristics

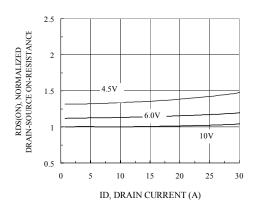


Figure 3. On Resistance Vs Vgs Voltage

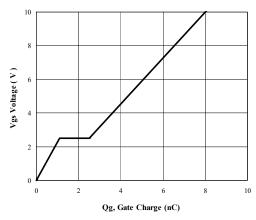


Figure 5. Gate Charge Characteristics

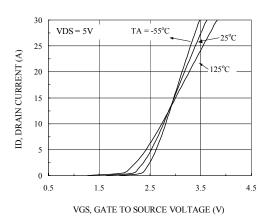


Figure 2. Body Diode Forward Voltage Variation with Source Current and Temperature

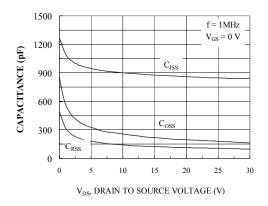


Figure 4. Capacitance Characteristics

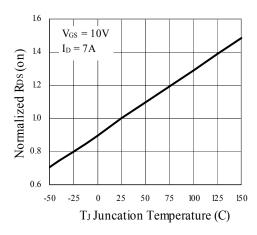


Figure 6. On-Resistance Variation with Temperature

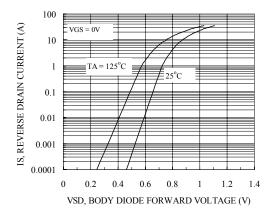
### Typical Electrical Characteristics (N-Channel)

0.1

0.08

0.06

0.04



RDS(ON), ON-RESISTANCE (OHM  $TA = 25^{\circ}C$ 0.02 10 6 VGS, GATE TO SOURCE VOLTAGE (V)

Figure 7. Transfer Characteristics

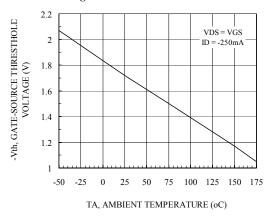


Figure 8. On-Resistance with Gate to Source Voltage

ID = 7 A

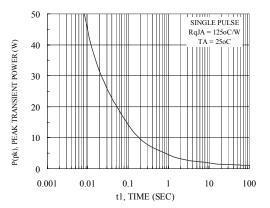


Figure 9. Vth Gate to Source Voltage Vs Temperature

Figure 10. Single Pulse Maximum Power Dissipation

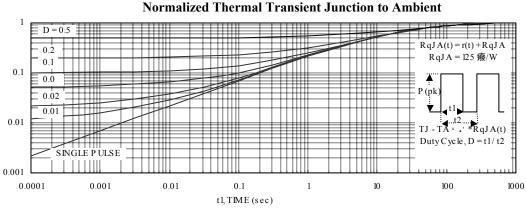
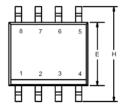


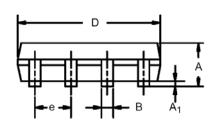
Figure 11. Transient Thermal Response Curve

**PRELIMINARY** 

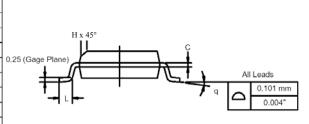
# Package Information

SO-8: 8LEAD





	MILLIMETERS		INC	HES
Dim	Min	Max	Min	Max
Α	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
В	0.35	0.51	0.014	0.020
С	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
е	1.27	BSC	0.050	BSC
Н	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°



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