P-Channel 40-V (D-S) MOSFET

Key Features:

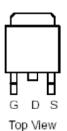
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
-40	5 @ V _{GS} = -10V	-90 ^a		
-40	6.6 @ V _{GS} = -4.5V	-90		





TO-263

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)						
Parameter			Limit	Units		
Drain-Source Voltage			-40	V		
Gate-Source Voltage		V _{GS}	±20	v		
Continuous Drain Current ^a	T _C =25°C	I _D	-90	A		
Pulsed Drain Current ^b		I _{DM}	-360	~		
Continuous Source Current (Diode Conduction) ^a	T _C =25°C	I _S	-90	А		
Power Dissipation ^a	T _C =25°C	PD	300	W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Ambient °	$R_{ extsf{ heta}JA}$	62.5	°C/W			
Maximum Junction-to-Case	$R_{ extsf{ heta}JC}$	0.5	C/W			

Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

Parameter	Symbol	Symbol Test Conditions		Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \text{ uA}$	-1			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current		$V_{DS} = -32 \text{ V}, V_{GS} = 0 \text{ V}$			-1	uA		
Zero Gale Voltage Drain Gurrent	I _{DSS}	$V_{DS} = -32 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-10	uA		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	-135			А		
Drain Course On Desistance ^a	r	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -20 \text{ A}$		5		mΩ		
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -18 \text{ A}$			6.6	11152		
Forward Transconductance ^a	g _{fs}	$V_{DS} = -20 \text{ V}, \text{ I}_{D} = -20 \text{ A}$		68		S		
Diode Forward Voltage ^a	V_{SD}	$I_{S} = -45 \text{ A}, V_{GS} = 0 \text{ V}$		-0.89		V		
		Dynamic ^b						
Total Gate Charge	Qg	V = -20 V V = -4 5 V		108				
Gate-Source Charge	Q _{gs}	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V},$ $I_{D} = -2 \text{ A}$		27		nC		
Gate-Drain Charge	Q_{gd}	10 - 27		34				
Turn-On Delay Time	t _{d(on)}	$V_{DS} = -20 \text{ V}, \text{ R}_{L} = 10 \Omega,$		30		ns		
Rise Time	t _r	$V_{DS} = -20$ V, $K_{L} = -10$ Ω, $I_{D} = -2$ A,		27				
Turn-Off Delay Time	t _{d(off)}	$V_{\text{GEN}} = -10 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$		306				
Fall Time	t _f	$V_{\text{GEN}} = 10$ V, $N_{\text{GEN}} = 0.22$		104				
Input Capacitance	C _{iss}			6488				
Output Capacitance	C _{oss}	V_{DS} = -20 V, V_{GS} = 0 V, f = 1 Mhz		556		pF		
Reverse Transfer Capacitance	C _{rss}			503				

Notes

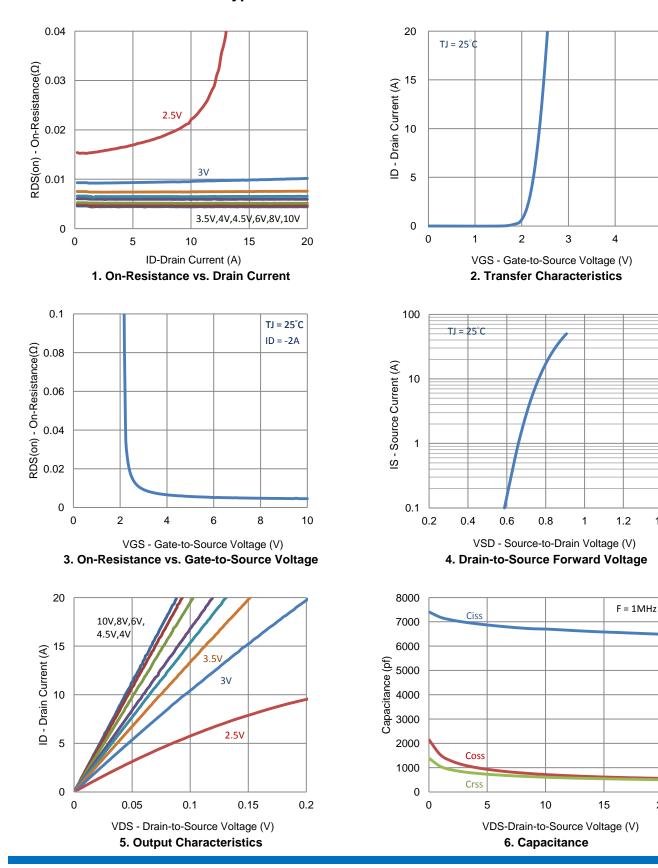
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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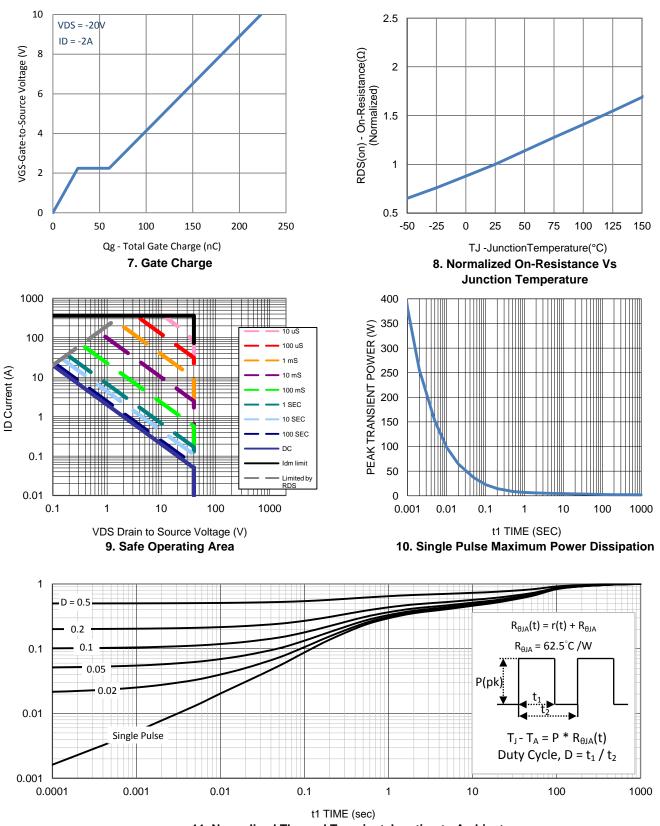
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1.4

20



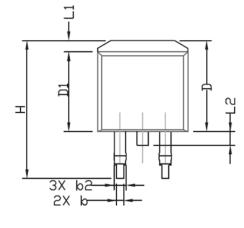
Typical Electrical Characteristics

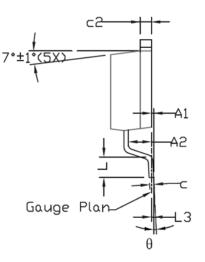


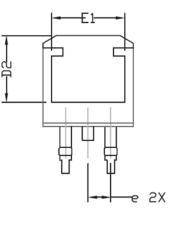
Typical Electrical Characteristics

11. Normalized Thermal Transient Junction to Ambient

Package Information







	DIMENS	IONAL F	REQMTS	INCH	INCHES REQMTS		
SYMBOL	MIN	NDM	MAX	MIN	NDM	MAX	
A	4,30	4.57	4,72	0.169	0.180	0.186	
A1	0		0.25	0		0.010	
A2	2,47	2,57	2,67	0.097	0.101	0.105	
b	0.69	0.813	0.94	0.027	0.032	0.037	
b2	1,17	1.27	1,45	0.046	0.050	0.057	
С	0.48	0,50	0.60	0.019	0.020	0.024	
c2	1,17	1.27	1.37	0.046	0.050	0.054	
D	9,80	10.05	10,30	0.386	0,396	0.406	
D1	8,64	8.78	9,65	0.340	0.346	0.380	
D2	7,12	7.37	7,62	0.280	0.290	0.300	
E	9,70	10.15	10.54	0.382	0.400	0.415	
E1	8,00	8,20	8,40	0.315	0,323	0.331	
e	2.	54 BSC		0.100 BSC			
н	14.99	15.24	15,49	0.590	0.600	0.610	
L	1.78	2.29	2,79	0.070	0.090	0.110	
L1	1.02	1.27	1.52	0.040	0.050	0.060	
L2			1.75			0.069	
L3		0,254			0.010		
θ	0*		8*	0*		8*	