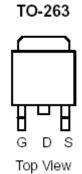
N-Channel 60-V (D-S) MOSFET

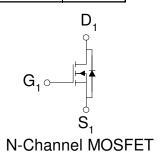
These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management such as computers, printers, and power supplies.

•	Low $r_{DS(on)}$ provides higher efficiency and
	extends battery life

- Low thermal impedance copper leadframe TO-263 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)} m(\Omega)$	I _D (A)	
60	19 @ $V_{GS} = 10V$	90°	
00	$24 @ V_{GS} = 4.5V$	90	





ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage			60	V
Gate-Source Voltage			±20	'
Continuous Drain Current ^a	$T_C=25^{\circ}C$	I_D	90	_
Pulsed Drain Current ^b		I_{DM}	240	A
Continuous Source Current (Diode Conduction) ^a			90	A
Power Dissipation ^a	T _C =25°C	P_{D}	300	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	R _{0JA}	62.5	°C/W	
Maximum Junction-to-Case	$R_{ heta JC}$	0.5	°C/W	

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Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature

Analog Power AM90N06-19B

SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)							
Downers to u	Symbol	Test Conditions	Limits			TT .4	
Parameter			Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mathrm{uA}$	1			V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			±100	nA	
Zara Cata Valtaga Drain Current	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	uА	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
Dig C Di A		$V_{GS} = 10 \text{ V}, I_{D} = 2 \text{ A}$			19	mΩ	
Drain-Source On-Resistance ^A	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2 \text{ A}$			25		
Forward Tranconductance ^A	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 2 \text{ A}$		30		S	
Diode Forward Voltage	V_{SD}	$I_S = 2 A$, $V_{GS} = 0 V$		1.1		V	
Dynamic ^b	•				-	-	
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}. V_{GS} = 4.5 \text{ V}.$		20			
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 2 \text{ A}$		5		nC	
Gate-Drain Charge	Q_{gd}			10			
Turn-On Delay Time	t _{d(on)}			6			
Rise Time	t _r	$V_{DD} = 25 \text{ V}, R_L = 25 \Omega$, $I_D = 34 \text{ A}$		6		nS	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 10 \text{ V}$		50		ns	
Fall-Time	t_{f}			20			

Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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