N-Channel 60-V (D-S) MOSFET

Key Features:

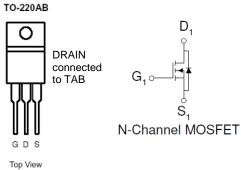
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- PoE Power Sourcing Equipment
- PoE Powered Devices
- Telecom DC/DC converters
- · White LED boost converters

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _D (A)	
60	9.9 @ V _{GS} = 10V	90°a	
	13 @ V _{GS} = 4.5V	90"	





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			60	V		
Gate-Source Voltage			±20	V		
Continuous Drain Current ^a	T _C =25°C	I _D	90 A			
Pulsed Drain Current ^b		I _{DM}	240			
Continuous Source Current (Diode Conduction) a			90	Α		
Power Dissipation ^a	T _C =25°C	P_{D}	120	W		
Operating Junction and Storage Temperature Range			-55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	62.5	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	1.25	C/VV		

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Notes

- a. Package limited
- b. Pulse width limited by maximum junction temperature

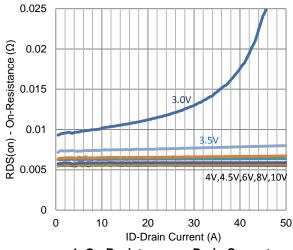
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1		3.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Brain Current	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25		
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_{D} = 30 \text{ A}$			9.9	mΩ	
	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$			13		
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		30		S	
Diode Forward Voltage	V_{SD}	$I_{S} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		0.8		V	
Dynamic							
Total Gate Charge	Q_g	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		77		nC	
Gate-Source Charge	Q_gs			21			
Gate-Drain Charge	Q_{gd}			40			
Turn-On Delay Time	t _{d(on)}			23			
Rise Time	t _r	$V_{DD} = 30 \text{ V}, R_L = 1.5 \Omega, I_D = 20 \text{ A}, \ V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		80		ns	
Turn-Off Delay Time	$t_{d(off)}$			226			
Fall-Time	t _f			99			
Input Capacitance	C _{iss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{MHz}$		5887			
Output Capacitance	C _{oss}			567		pF	
Reverse Transfer Capacitance	C_{rss}			352			

Notes

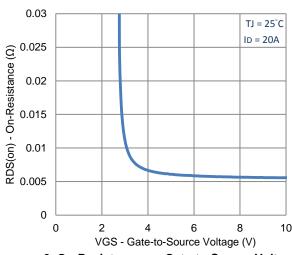
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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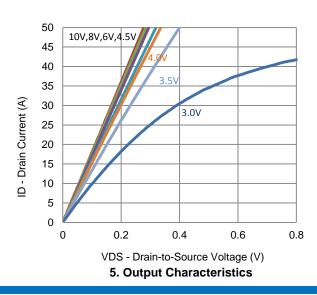
Typical Electrical Characteristics

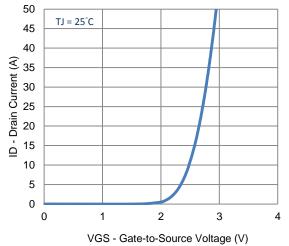


1. On-Resistance vs. Drain Current

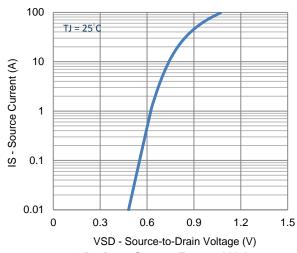


3. On-Resistance vs. Gate-to-Source Voltage

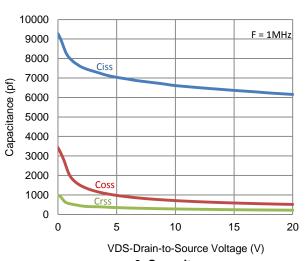




2. Transfer Characteristics

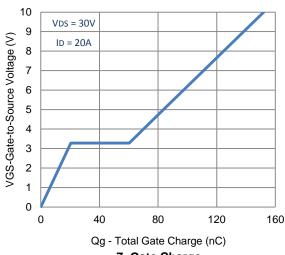


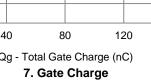
4. Drain-to-Source Forward Voltage

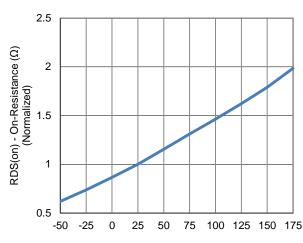


6. Capacitance

Typical Electrical Characteristics

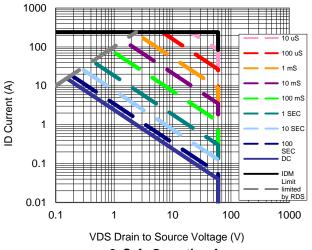




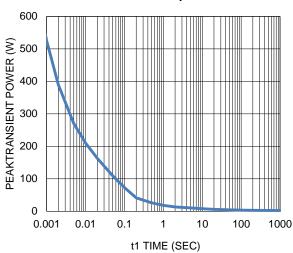


TJ - Junction Temperature (°C)

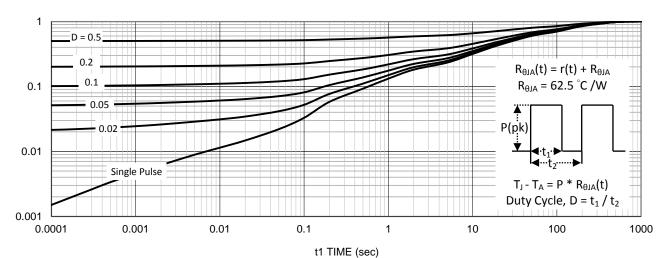




9. Safe Operating Area

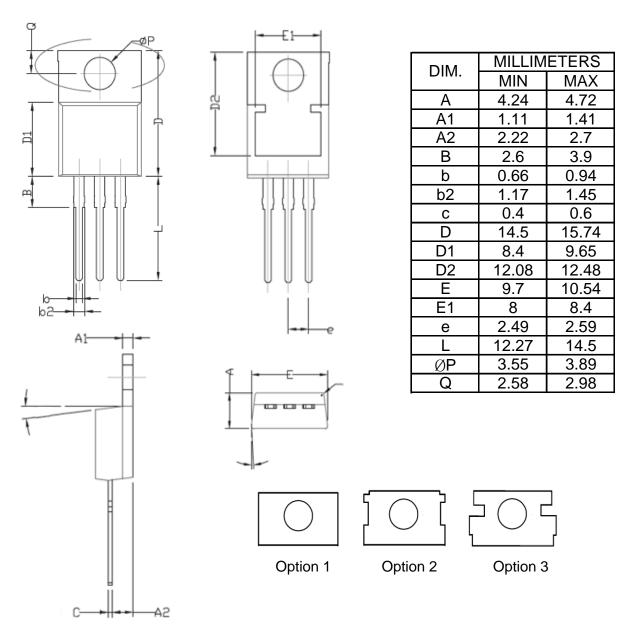


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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