N-Channel 60-V (D-S) MOSFET

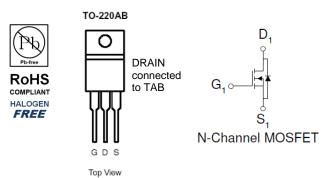
Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical .	Applica	ations:
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- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
60	$3 @ V_{GS} = 10V$	90 ^a	
	$4 @ V_{GS} = 5.5V$	90	



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V_{DS}	60	V	
Gate-Source Voltage		V_{GS}	±20		
Continuous Drain Current a	T _A =25°C	I _D	90	Α	
Pulsed Drain Current ^b		I _{DM}	360		
Continuous Source Current (Diode Conduction) a		I _S	90	Α	
Power Dissipation ^a	T _A =25°C	P_{D}	300	W	
Operating Junction and Storage Temperature Range		T_J , T_{stg}	-55 to 175	°C	

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	1	C/VV

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

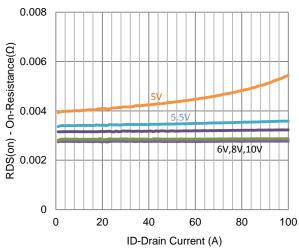
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
Zero Gate Voltage Brain Gurrent	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	
On-State Drain Current	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_{D} = 45 \text{ A}$			3	mΩ
	r _{DS(on)}	$V_{GS} = 5.5 \text{ V}, I_D = 44 \text{ A}$			4	
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		35		S
Diode Forward Voltage	V_{SD}	$I_{S} = 45 \text{ A}, V_{GS} = 0 \text{ V}$		0.84		V
		Dynamic				
Total Gate Charge	Q_g	$V_{DS} = 30 \text{ V}, V_{GS} = 5.5 \text{ V},$ $I_{D} = 20 \text{ A}$		161		nC
Gate-Source Charge	Q_gs			58		
Gate-Drain Charge	Q_gd	1D = 20 A		82		
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 30 \text{ V}, R_{L} = 1.5 \Omega,$ $I_{D} = 20 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		64		
Rise Time	t _r			112		ns
Turn-Off Delay Time	t _{d(off)}			276		
Fall Time	t _f			86		
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		33061		
Output Capacitance	C _{oss}			1181		pF
Reverse Transfer Capacitance	C_{rss}			1135		

Notes

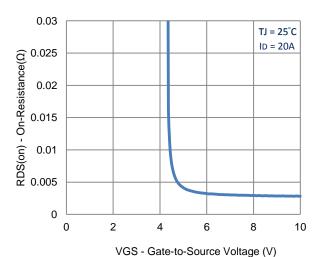
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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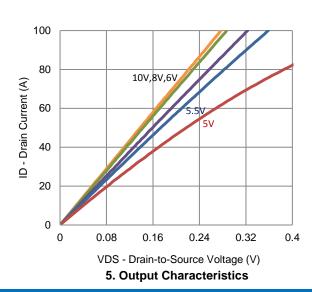
Typical Electrical Characteristics

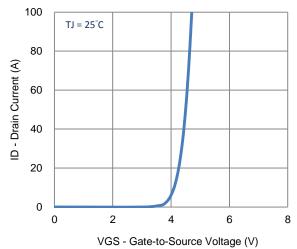


1. On-Resistance vs. Drain Current

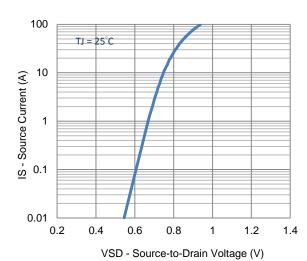


3. On-Resistance vs. Gate-to-Source Voltage

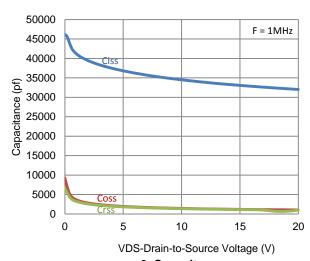




2. Transfer Characteristics

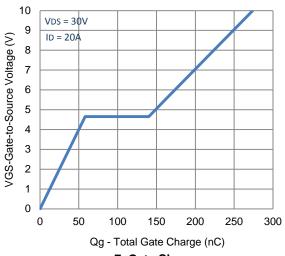


4. Drain-to-Source Forward Voltage

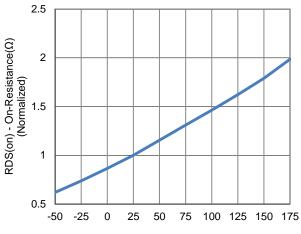


6. Capacitance

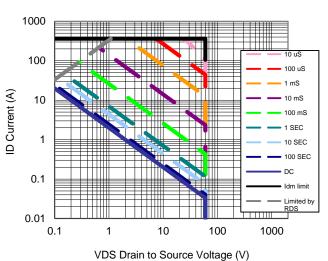
Typical Electrical Characteristics



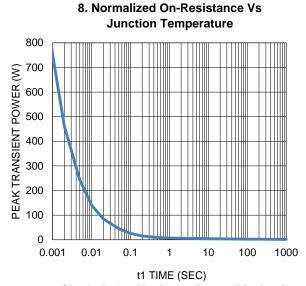
7. Gate Charge



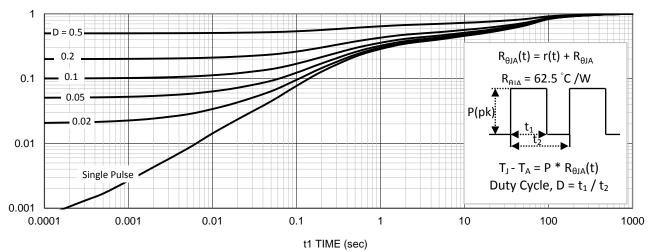
TJ -JunctionTemperature(°C)



9. Safe Operating Area



10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information

