# N-Channel 150-V (D-S) MOSFET

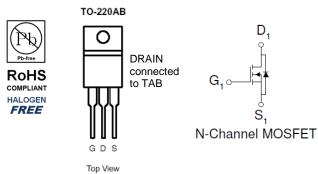
### **Key Features:**

- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

<b>Typical</b>	Applications	
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- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I <sub>D</sub> (A)	
150	48 @ V <sub>GS</sub> = 10V	65 <sup>a</sup>	
	54 @ V <sub>GS</sub> = 5.5V	65	



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		$V_{DS}$	150	V	
Gate-Source Voltage		$V_{GS}$	±20		
Continuous Drain Current a	T <sub>A</sub> =25°C	I <sub>D</sub>	65	Α	
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub> 260		^		
Continuous Source Current (Diode Conduction) a		I <sub>S</sub>	110	Α	
Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	$P_{D}$	300	W	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175	°C	

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>a</sup>	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	1	C/VV

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#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

### **Electrical Characteristics**

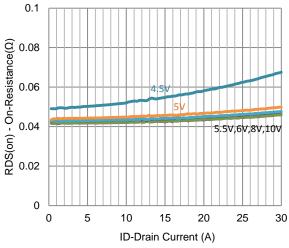
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
	Static					
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \text{ uA}$	1			V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	lass	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
Zero Gate Voltage Brain Gurrent	I <sub>DSS</sub>	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	
On-State Drain Current	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	130			Α
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$			48	mΩ
	r <sub>DS(on)</sub>	$V_{GS} = 5.5 \text{ V}, I_D = 19.2 \text{ A}$			54	
Forward Transconductance	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		30		S
Diode Forward Voltage	$V_{SD}$	$I_{S} = 55 \text{ A}, V_{GS} = 0 \text{ V}$		0.95		V
		Dynamic				
Total Gate Charge	$Q_g$	$V_{DS} = 75 \text{ V}, V_{GS} = 5.5 \text{ V},$		57		nC
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 75 \text{ V}, V_{GS} = 5.5 \text{ V},$ $I_{D} = 20 \text{ A}$		16		
Gate-Drain Charge	$Q_gd$			32		
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = 75 \text{ V}, R_1 = 3.8 \Omega,$		21		
Rise Time	t <sub>r</sub>	$V_{DS} = 75 \text{ V}, R_L = 3.6 \Omega,$ $I_D = 20 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		25		ne
Turn-Off Delay Time	$t_{d(off)}$			132		ns
Fall Time	t <sub>f</sub>			45		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		4544		
Output Capacitance	C <sub>oss</sub>			257		pF
Reverse Transfer Capacitance	$C_{rss}$			256		

#### **Notes**

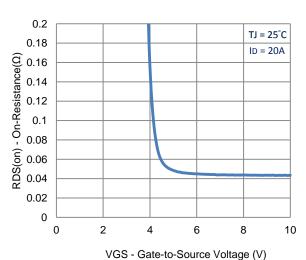
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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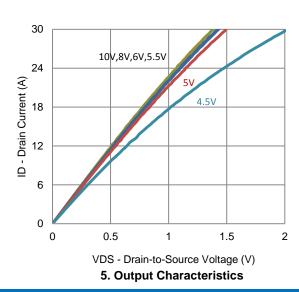
### **Typical Electrical Characteristics**

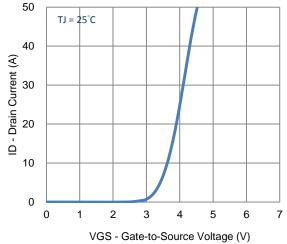


#### 1. On-Resistance vs. Drain Current

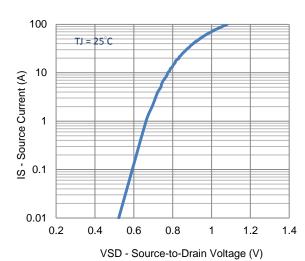


3. On-Resistance vs. Gate-to-Source Voltage

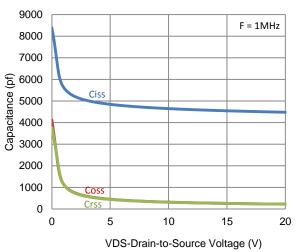




2. Transfer Characteristics



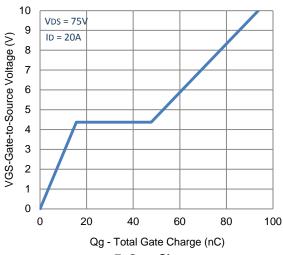
4. Drain-to-Source Forward Voltage

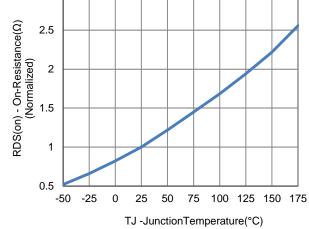


6. Capacitance

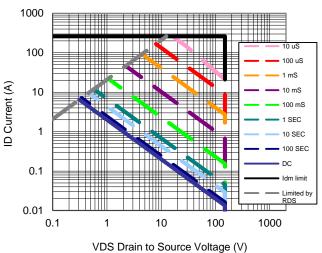
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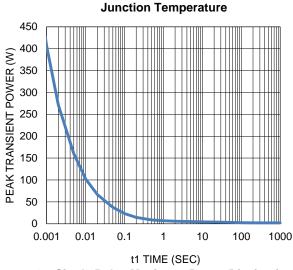
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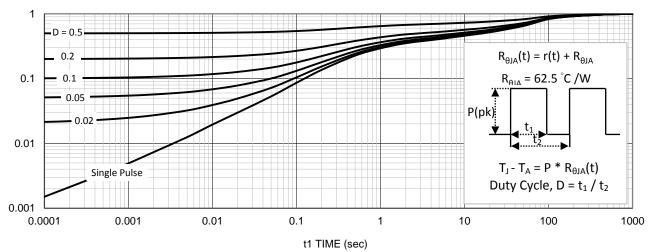
7. Gate Charge 8. Normalized On-Resistance Vs





9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

## **Package Information**

