

Dual P-Channel 30-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

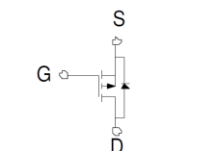
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
-30	50 @ $V_{GS} = -10V$	-4.2
	72 @ $V_{GS} = -4.5V$	-3.5



RoHS
COMPLIANT
HALOGEN
FREE



TSSOP-8
Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	I_D	-4.2	A
	$T_A = 70^\circ\text{C}$		-3.3	
Pulsed Drain Current ^b		I_{DM}	-20	
Continuous Source Current (Diode Conduction) ^a		I_S	-1.7	A
Power Dissipation ^a	$T_A = 25^\circ\text{C}$	P_D	1.3	W
	$T_A = 70^\circ\text{C}$		0.8	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 10 \text{ sec}$	$R_{\theta JA}$	100	$^\circ\text{C/W}$
	Steady State		166	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

Electrical Characteristics

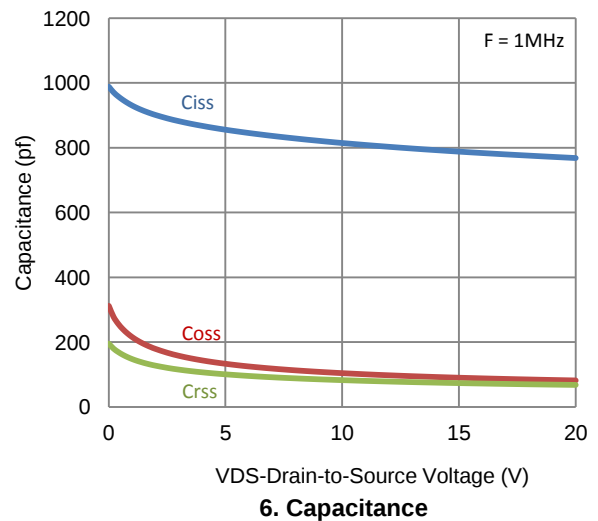
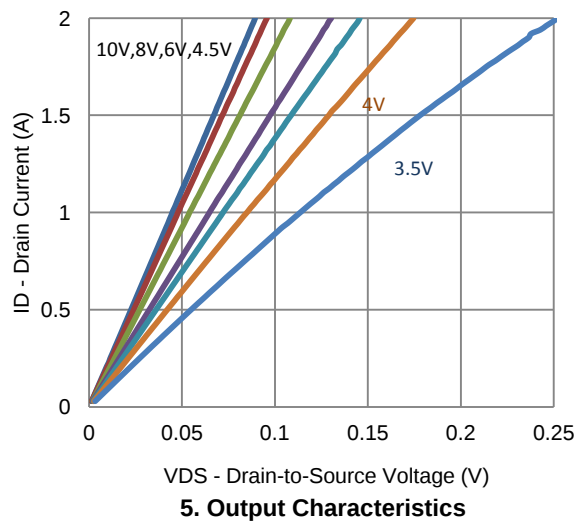
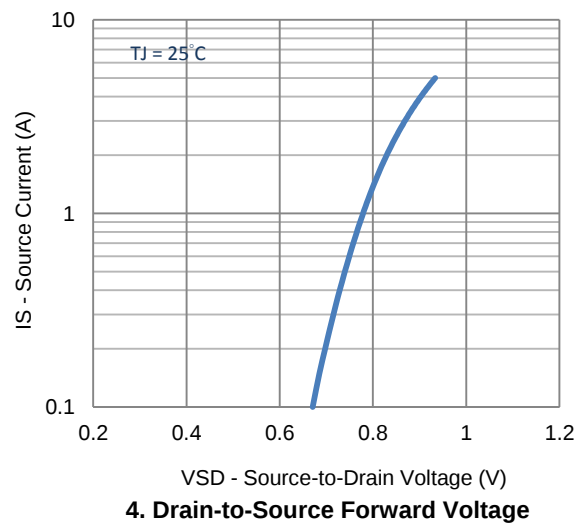
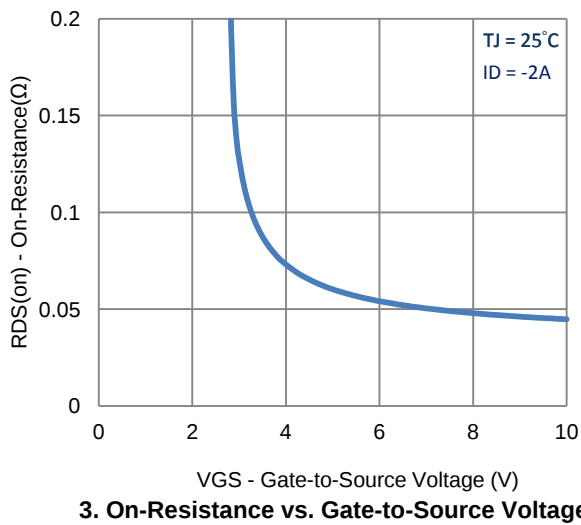
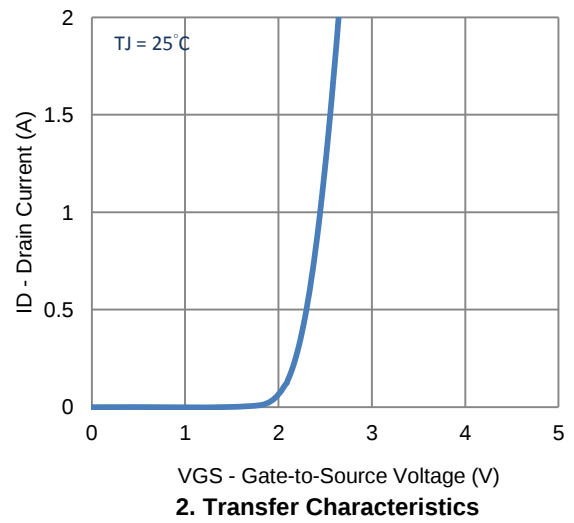
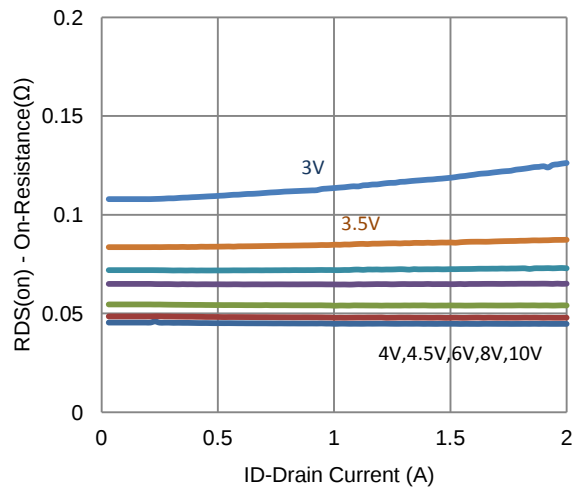
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24 V, V_{GS} = 0 V$			-1	μA
		$V_{DS} = -24 V, V_{GS} = 0 V, T_J = 55^\circ C$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5 V, V_{GS} = -10 V$	-6.3			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = -10 V, I_D = -2 A$			50	m Ω
		$V_{GS} = -4.5 V, I_D = -1.6 A$			72	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15 V, I_D = -2 A$		6		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -0.9 A, V_{GS} = 0 V$		-0.77		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = -15 V, V_{GS} = -4.5 V,$ $I_D = -2 A$		9		nC
Gate-Source Charge	Q_{gs}			3.3		
Gate-Drain Charge	Q_{gd}			2.8		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -15 V, R_L = 7.5 \Omega,$ $I_D = -2 A,$ $V_{GEN} = -10 V, R_{GEN} = 6 \Omega$		6		ns
Rise Time	t_r			7		
Turn-Off Delay Time	$t_{d(off)}$			44		
Fall Time	t_f			19		
Input Capacitance	C_{iss}	$V_{DS} = -15 V, V_{GS} = 0 V, f = 1 \text{ Mhz}$		787		pF
Output Capacitance	C_{oss}			90		
Reverse Transfer Capacitance	C_{rss}			73		

Notes

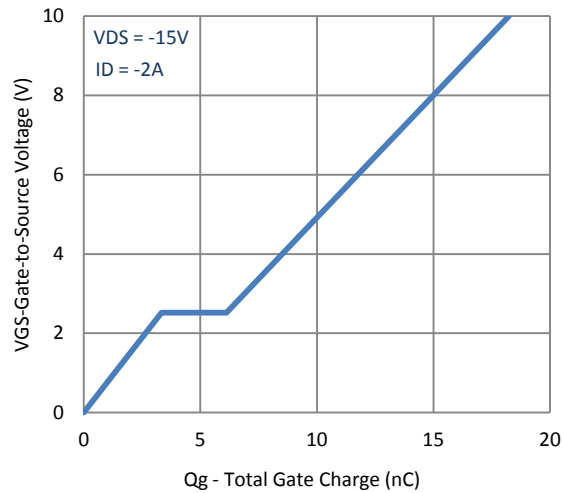
- a. Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

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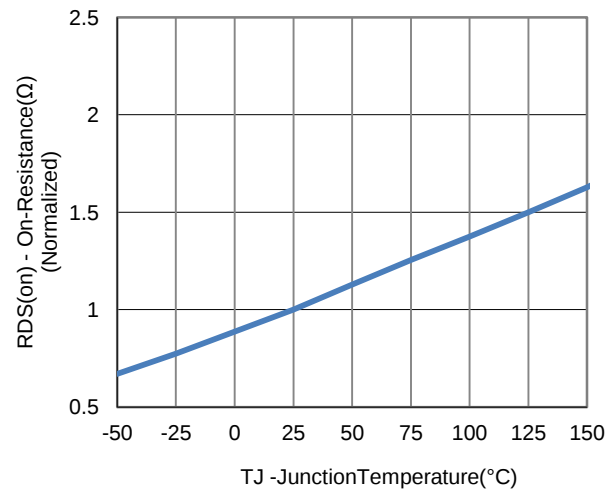
Typical Electrical Characteristics



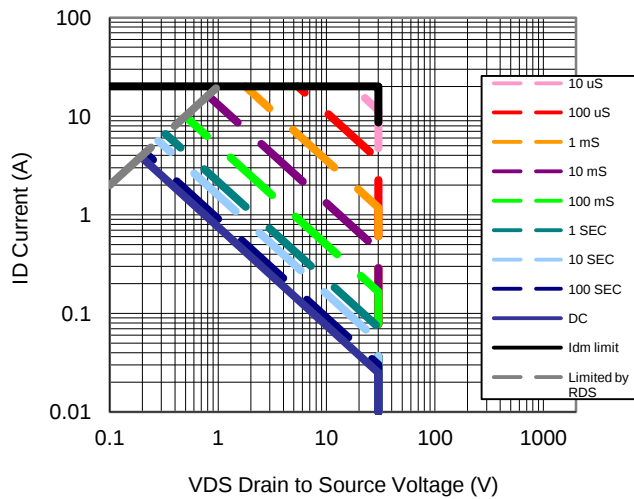
Typical Electrical Characteristics



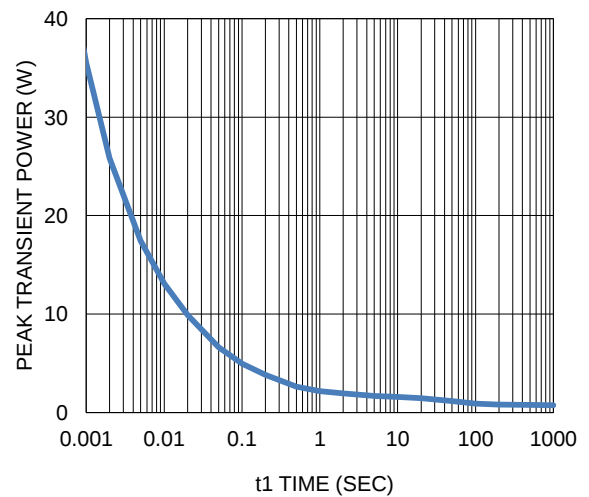
7. Gate Charge



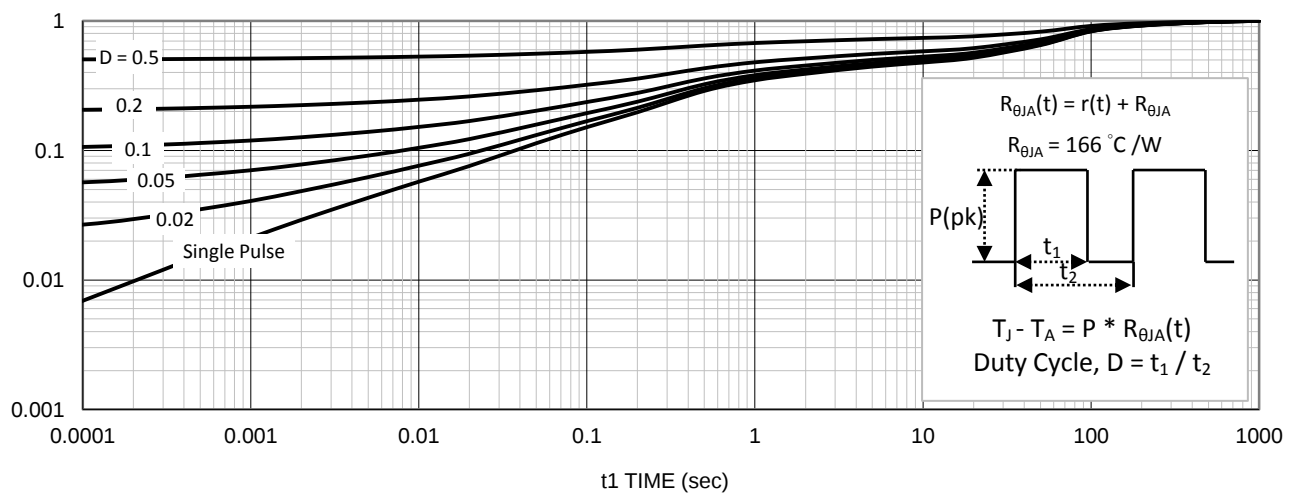
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area



10. Single Pulse Maximum Power Dissipation

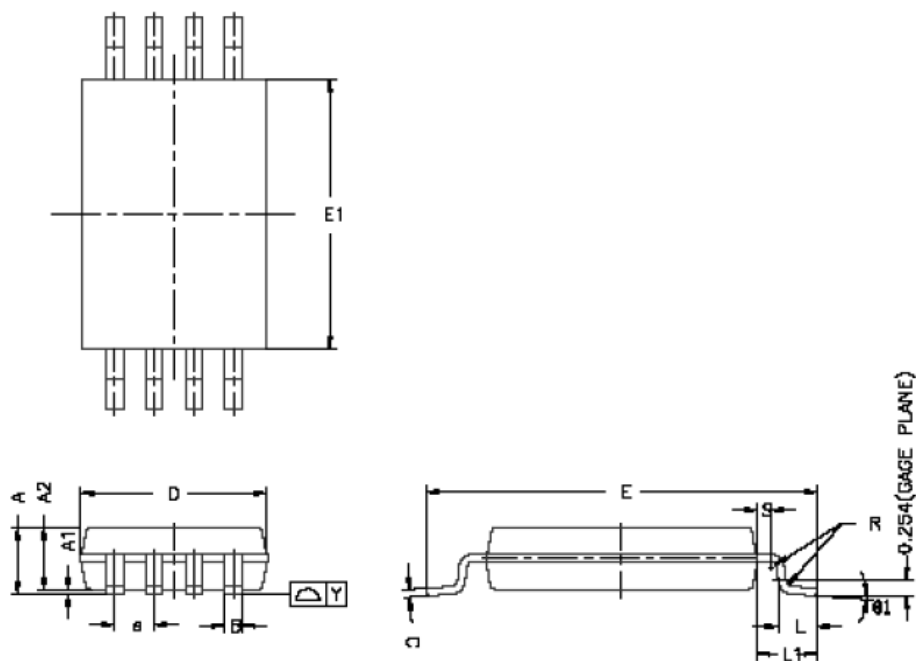


11. Normalized Thermal Transient Junction to Ambient

Package Information

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TSSOP-8: 8LEAD



DIM.	MILLIMETERS		
	MIN.	NDM.	MAX.
A	1.05	1.10	1.20
A(1)	0.05	0.10	0.15
A(2)	0.99	1.02	1.05
B	0.19	0.25	0.30
C	---	0.127	---
D	2.80	3.00	3.10
E	6.20	6.40	6.60
E1	4.30	4.40	4.50
B	0.65±SC		
L	0.45	0.60	0.75
L1	0.90	1.00	1.10
Y	---	---	0.10
Ø1	Ø	4°	Ø
R	0.09	---	---
S	0.20	---	---