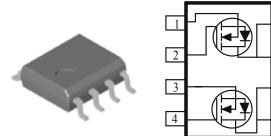
Dual N-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY					
$V_{DS}(V)$	$r_{DS(on)} m(\Omega)$ $I_D(A)$				
30	$13.5 @ V_{GS} = 10V$	10			
	$20 @ V_{GS} = 4.5V$	8			

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8 saves board space
- Fast switching speed
- High performance trench technology



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter			Limit	Units		
Drain-Source Voltage			30	V		
Gate-Source Voltage			±20	v		
Continuo Dario Communt ^a	$T_A=25^{\circ}C$	ī	10			
Continuous Drain Current ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	¹ D	8.2	A		
Pulsed Drain Current ^b			±50			
Continuous Source Current (Diode Conduction) ^a	I_S	2.3	A			
D D:	$T_A=25^{\circ}C$	D	2.1	W		
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$]¹ D	1.3			
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Case ^a	$t \le 5 \sec$	$R_{ heta JC}$	40	°C/W		
Maximum Junction-to-Ambient ^a	t <= 5 sec	$R_{\theta JA}$	60	°C/W		

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

SPECIFICATIONS (T _A = 25°C	J CITELOG (STILKWISE NOTED)	1	Ŧ		
Parameter	Symbol	Test Conditions	Min	Limits		Unit
1 41 41.10001	~J 1110 01	Symbol Test Conditions		Typ	Max	01110
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \text{ uA}$	30			V
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			·
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
Zero Gate Voltage Drain Current	¹ DSS	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	
On-State Drain Current ^A	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			A
		$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$			13.5	mΩ
Drain-Source On-Resistance ^A	$r_{DS(on)}$	$V_{GS} = 4.5 \text{ V}, I_D = 8 \text{ A}$			20	
		$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}, T_J = 55^{\circ}\text{C}$			15	
Forward Tranconductance ^A	\mathbf{g}_{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 10 \text{ A}$		40		S
Diode Forward Voltage	V_{SD}	$I_S = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.7		V
Pulsed Source Current (Body Diode) ^A	I_{SM}			5		A
Dynamic ^b						
Total Gate Charge	Q_{g}	V - 15 V V - 5 V		20		
Gate-Source Charge	Q_{gs}	$\mathbf{v}_{\mathrm{DS}} = 13 \mathbf{v}, \mathbf{v}_{\mathrm{GS}} = 3 \mathbf{v},$		7.0		пC
Gate-Drain Charge	Q_{gd}	$I_D = 10 \text{ A}$		7.0		
Turn-On Delay Time	t _{d(on)}			20		
Rise Time	t _r	$V_{DD} = 25 \text{ V}, R_L = 25 \Omega, I_D = 1 \text{ A},$		9		nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}$		70		113
Fall-Time	t _f			20		

Notes

- a. Pulse test: $PW \le 300us duty cycle \le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Typical Electrical Characteristics (N-Channel)

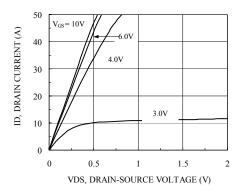


Figure 1. On-Region Characteristics

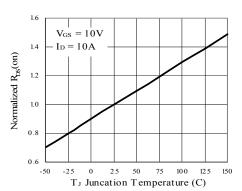


Figure 3. On-Resistance Variation with Temperature

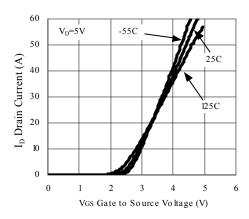


Figure 5. Transfer Characteristics

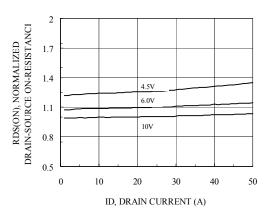


Figure 2. On-Resistance with Drain Current

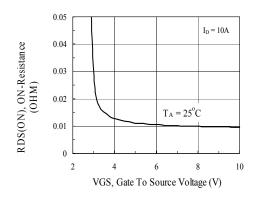


Figure 4. On-Resistance Variation with Gate to Source Voltage

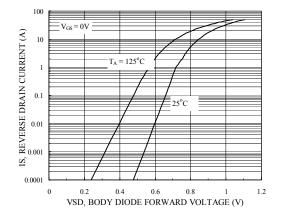


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

Typical Electrical Characteristics (N-Channel)

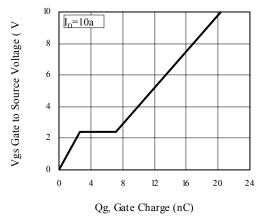


Figure 7. Gate Charge Characteristics

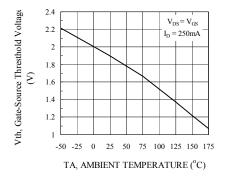


Figure 9. Threshold Vs Ambient Temperature

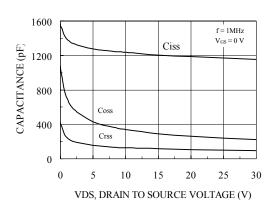


Figure 8. Capacitance Characteristics

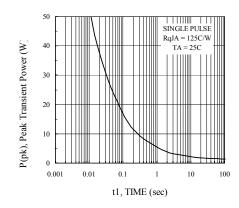


Figure 10. Single Pulse Maximum Power Dissipation

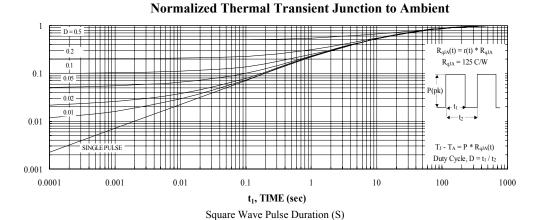
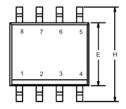
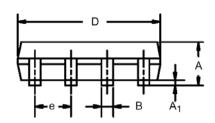


Figure 11. Transient Thermal Response Curve

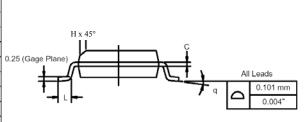
Package Information

SO-8: 8LEAD





	MILLIMETERS		INCHES	
Dim	Min	Max	Min	Max
Α	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
В	0.35	0.51	0.014	0.020
С	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
е	1.27 BSC		0.050 BSC	
Н	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°



Ordering information

• AM4910N-T1-XX

- A: Analog Power

- M: MOSFET

– 4910: Part number

- N: N-Channel

- T1: Tape & reel

- XX: Blank: Standard

PF: Leadfree