

N-Channel 150-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

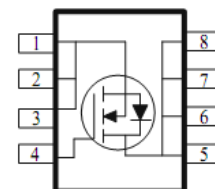
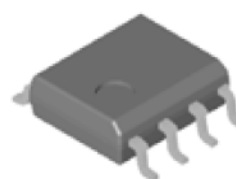
- PoE PSE and PD Circuits
- LED Inverter Circuits
- 48V-Input DC/DC Conversion Circuits

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
150	83 @ $V_{GS} = 10V$	5.0
	105 @ $V_{GS} = 5.5V$	4.5



RoHS
COMPLIANT
HALOGEN
FREE

SO-8



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	I_D	5.0	A
	$T_A = 70^\circ\text{C}$		4.3	
Pulsed Drain Current ^b		I_{DM}	20	
Continuous Source Current (Diode Conduction) ^a		I_S	4.5	A
Power Dissipation ^a	$T_A = 25^\circ\text{C}$	P_D	3.1	W
	$T_A = 70^\circ\text{C}$		2.2	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 10 \text{ sec}$	$R_{\theta JA}$	40	$^\circ\text{C/W}$
	Steady State		80	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 120 V, V_{GS} = 0 V$			1	μA
		$V_{DS} = 120 V, V_{GS} = 0 V, T_J = 55^\circ C$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 V, V_{GS} = 10 V$	7.5			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = 10 V, I_D = 2 A$			83	m Ω
		$V_{GS} = 6.5 V, I_D = 1.6 A$			105	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 V, I_D = 2 A$		12		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 2 A, V_{GS} = 0 V$		0.72		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = 75 V, V_{GS} = 6.5 V,$ $I_D = 2 A$		11		nC
Gate-Source Charge	Q_{gs}			2.5		
Gate-Drain Charge	Q_{gd}			5.7		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 75 V, R_L = 37.5 \Omega,$ $I_D = 2 A,$ $V_{GEN} = 10 V, R_{GEN} = 6 \Omega$		8		ns
Rise Time	t_r			11		
Turn-Off Delay Time	$t_{d(off)}$			28		
Fall Time	t_f			14		
Input Capacitance	C_{iss}	$V_{DS} = 15 V, V_{GS} = 0 V, f = 1 \text{ Mhz}$		726		pF
Output Capacitance	C_{oss}			105		
Reverse Transfer Capacitance	C_{rss}			64		

Notes

- a. Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

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