N-Channel 60-V (D-S) MOSFET

Key Features:

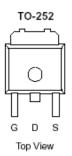
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
60	94 @ V _{GS} = 10V	19	
00	109 @ V _{GS} = 4.5V	18	





ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter			Limit	Units	
Drain-Source Voltage			60	V	
Gate-Source Voltage	V_{GS}	±20	v		
Continuous Drain Current ^a	T _A =25°C	I _D	19	А	
Pulsed Drain Current ^b			75	~	
Continuous Source Current (Diode Conduction) ^a	I _S	42	А		
Power Dissipation ^a	T _A =25°C	PD	50	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	R _{θJA}	40	°C/W		
Maximum Junction-to-Case	$R_{ extsf{ heta}JC}$	3	0/00		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

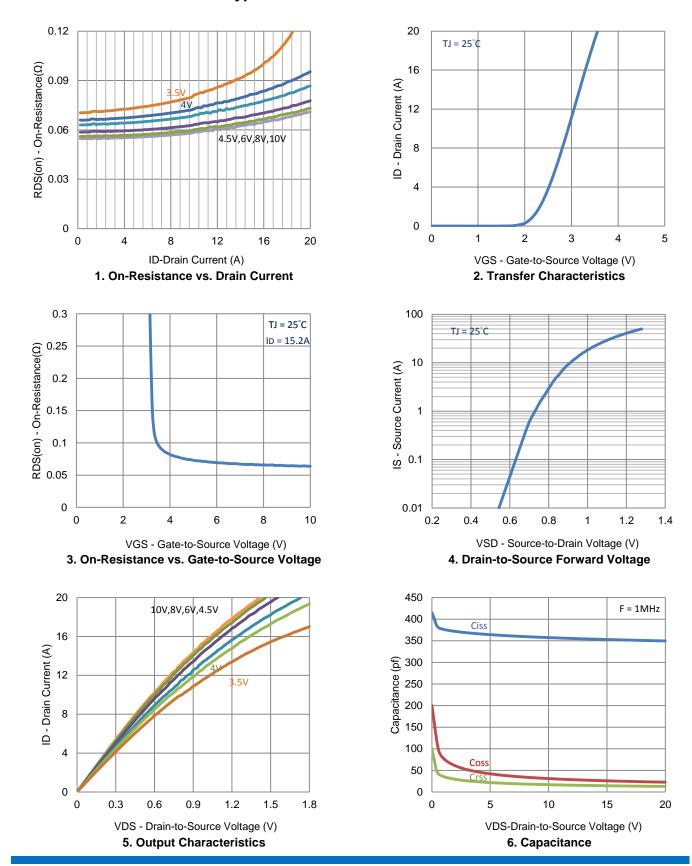
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1 uA		
	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$, V _{GS} = 0 V, T _J = 55°C		25	uA	
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	30			А	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 15.2 \text{ A}$			94	mΩ	
Drain-Source On-Resistance	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_{D} = 14 \text{ A}$			109		
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 15.2 \text{ A}$		20		S	
Diode Forward Voltage	V _{SD}	$I_{S} = 21 \text{ A}, V_{GS} = 0 \text{ V}$		1.03		V	
		Dynamic					
Total Gate Charge	Q _g	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$		5.1			
Gate-Source Charge	Q _{gs}	$V_{DS} = 30 V, V_{GS} = 4.3 V,$ $I_{D} = 15.2 A$		2.3		nC	
Gate-Drain Charge	Q _{gd}	10 = 10.2 A		2.0			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 30 \text{ V}, \text{ R}_{L} = 2 \Omega,$		4			
Rise Time	t _r	$V_{DS} = 30^{\circ} V, K_{L} = 2.02,$ $I_{D} = 15.2 \text{ A},$		9		200	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		17		ns	
Fall Time	t _f	$v_{\text{GEN}} = 10 v, N_{\text{GEN}} = 0.02$		19			
Input Capacitance	C _{iss}			353			
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		26		pF	
Reverse Transfer Capacitance	C _{rss}]		14			

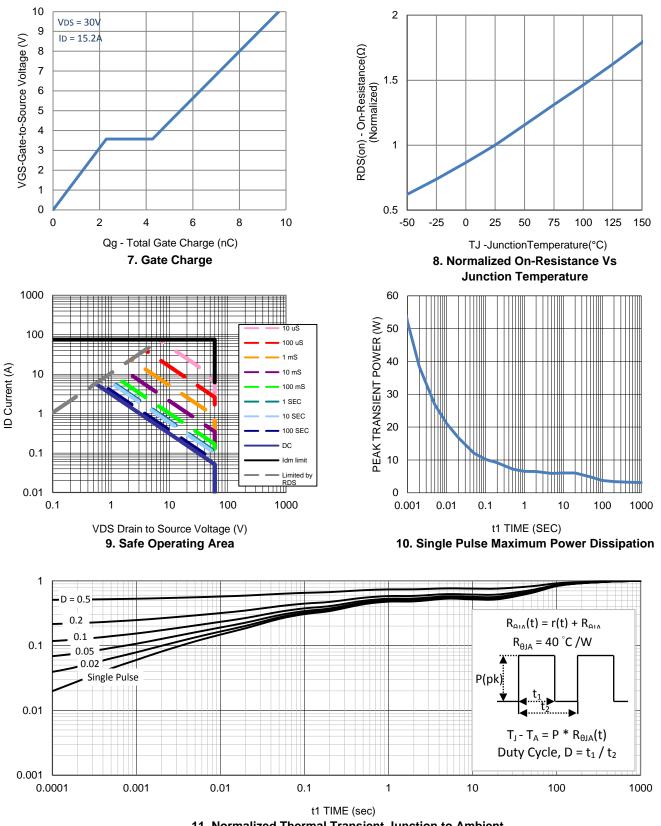
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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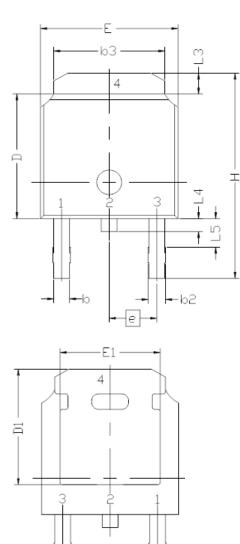
Typical Electrical Characteristics



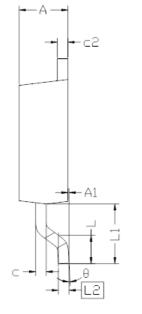
Typical Electrical Characteristics

11. Normalized Thermal Transient Junction to Ambient

Package Information



SINGLE ROWNEW



	DIMENS:		REQMES
SYMBOL	MIN	NDM	MAX
E	6.40	6,60	6.731
L	1.40	1.52	1.77
L1 2		.743 RI	
L2	0.	508 BS	_
L3	0.89		1.27
L4	0.64		1.01
L5			
D	6.00	6.10	6.223
Н	9.40	10.00	10.40
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e A	2.	286 BS	
A	2.20	2.30	2.38
A1	0		0.127
C	0.45	0.50	0.60
C2	0.45	0.50	0,58
D1	5.30		
E1	4.40		
A	0°		10°



- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.