N-Channel 100-V (D-S) MOSFET

Key Features:

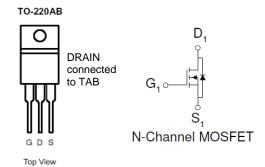
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- · LED Inverter Circuits
- DC/DC Conversion Circuits
- Motor drives

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
100	$6.5 @ V_{GS} = 10V$	180 ^a	
100	$8.5 @ V_{GS} = 5.5V$	180	





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage		V_{DS}	100	V		
Gate-Source Voltage			±20	V		
Continuous Drain Current ^a	T _C =25°C	I_D	180	Α		
Pulsed Drain Current ^b		I _{DM}	700	Λ		
Continuous Source Current (Diode Conduction) a	T _C =25°C	I _S	180	Α		
Power Dissipation	T _C =25°C	P_D	300	W		
Single Pulse Avalanche Energy ^d		E _{AS}	500	mJ		
Operating Junction and Storage Temperature Range			-55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W			
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV			

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Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.
- d. Tj=25 °C, L=0.51mH, I_D =45A, V_{DD} =50V

Electrical Characteristics

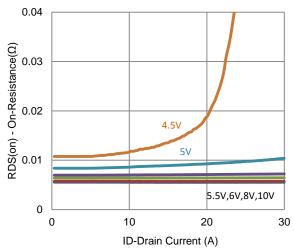
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
	I _{DSS}	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	220			Α	
Drain-Source On-Resistance ^a	r	$V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$			6.5	mΩ	
	r _{DS(on)}	$V_{GS} = 5.5 \text{ V}, I_D = 16 \text{ A}$			8.5		
Forward Transconductance a	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		18		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 90 \text{ A}, V_{GS} = 0 \text{ V}$		1		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 50 \text{ V}, V_{GS} = 5.5 \text{ V},$ $I_{D} = 20 \text{ A}$		78		nC	
Gate-Source Charge	Q_gs			42			
Gate-Drain Charge	Q_gd	1D = 20 A		27			
Turn-On Delay Time	t _{d(on)}	V_{DS} = 50 V, R_{L} = 2.5 Ω, I_{D} = 20 A, V_{GEN} = 10 V, R_{GEN} = 6 Ω		49		ns	
Rise Time	t _r			37			
Turn-Off Delay Time	$t_{d(off)}$			117			
Fall Time	t _f			51			
Input Capacitance	C_{iss}			8681			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		730		pF	
Reverse Transfer Capacitance	C_{rss}			331			

Notes

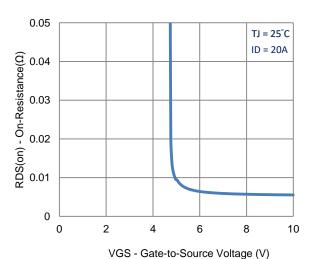
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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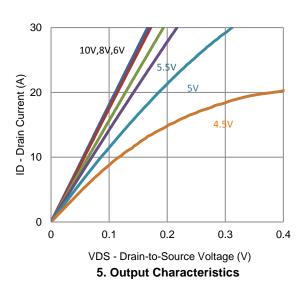
Typical Electrical Characteristics

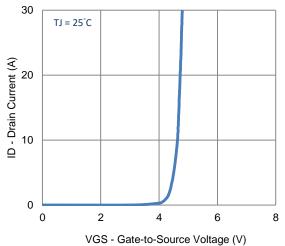


1. On-Resistance vs. Drain Current

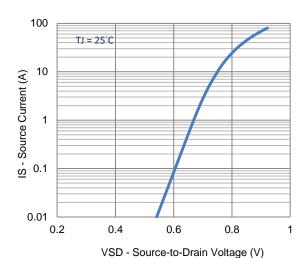


3. On-Resistance vs. Gate-to-Source Voltage

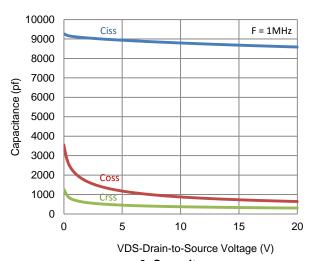




2. Transfer Characteristics

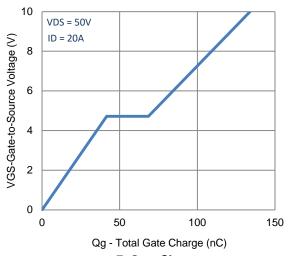


4. Drain-to-Source Forward Voltage

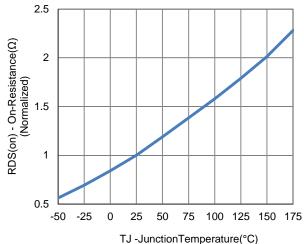


6. Capacitance

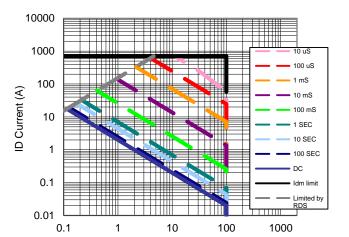
Typical Electrical Characteristics





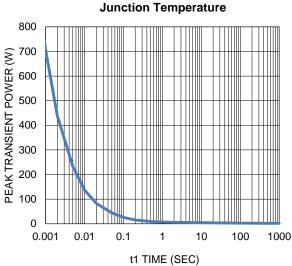


8. Normalized On-Resistance Vs

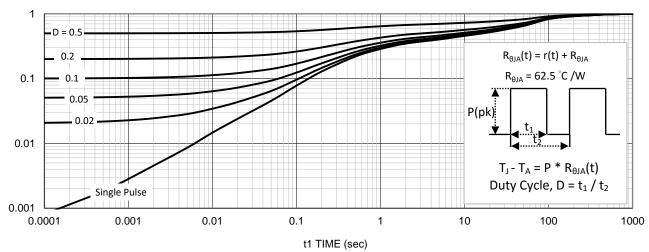


VDS Drain to Source Voltage (V)

9. Safe Operating Area

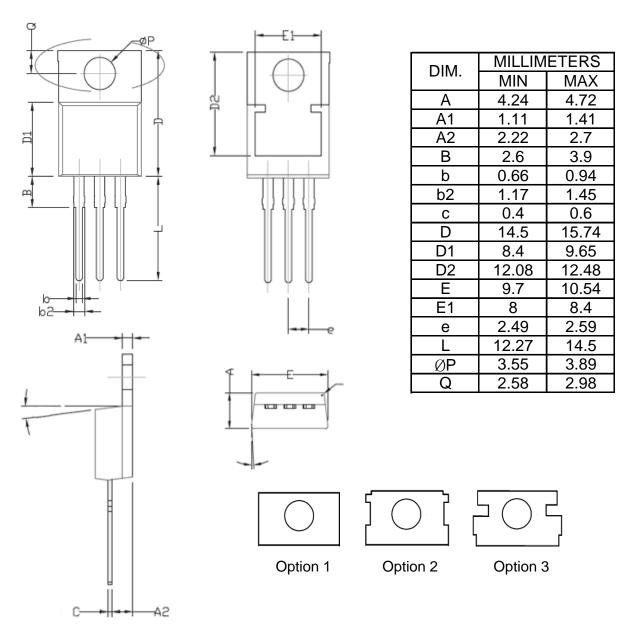


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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