N-Channel 1000-V (D-S) MOSFET

Key Features:

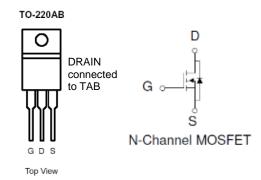
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
1000	1167 @ V _{GS} = 10V	10 ^a	
	1180 @ V _{GS} = 6.5V	10	





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V_{DS}	1000	V	
Gate-Source Voltage		V_{GS}	±20		
Continuous Drain Current a	T _C =25°C	I_D	10	Α	
Pulsed Drain Current b		I _{DM}	50		
Continuous Source Current (Diode Conduction) ^a T _C =25°C		I _S	3	Α	
Power Dissipation ^a	T _C =25°C	P_{D}	300	W	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV		

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Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

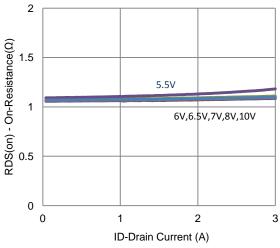
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	3			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	1	$V_{DS} = 800 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
	I _{DSS}	$V_{DS} = 800 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	12.5			Α	
Drain-Source On-Resistance ^a	r	$V_{GS} = 10 \text{ V}, I_{D} = 3 \text{ A}$			1167	mΩ	
	r _{DS(on)}	$V_{GS} = 6.5 \text{ V}, I_D = 2 \text{ A}$			1180		
Forward Transconductance a	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 3 \text{ A}$		10		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.72		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 100 \text{ V}, V_{GS} = 6.5 \text{ V},$ $I_{D} = 1 \text{ A}$		30		nC	
Gate-Source Charge	Q_{gs}			13			
Gate-Drain Charge	Q_gd			12			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 100 \text{ V}, R_L = 100 \Omega,$ $I_D = 1 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		27		ns	
Rise Time	t _r			4			
Turn-Off Delay Time	$t_{d(off)}$			53			
Fall Time	t _f			18			
Input Capacitance	C _{iss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		2558		pF	
Output Capacitance	C _{oss}			118			
Reverse Transfer Capacitance	C_{rss}			9			

Notes

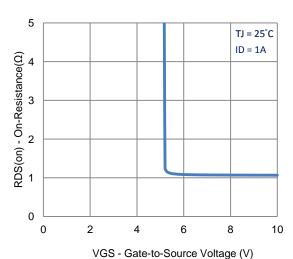
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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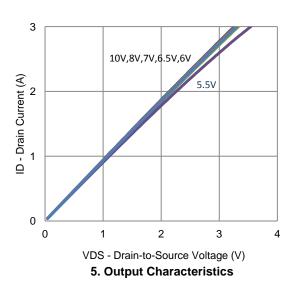
Typical Electrical Characteristics

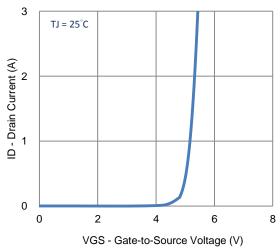


1. On-Resistance vs. Drain Current

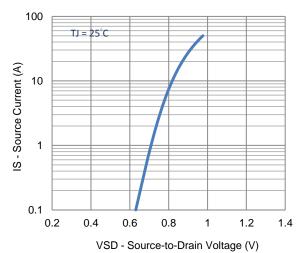


3. On-Resistance vs. Gate-to-Source Voltage

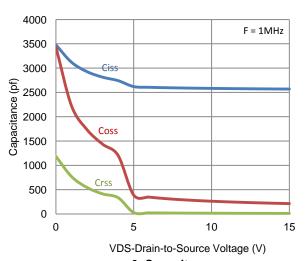




2. Transfer Characteristics

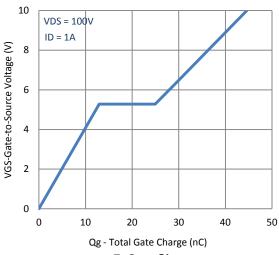


4. Drain-to-Source Forward Voltage

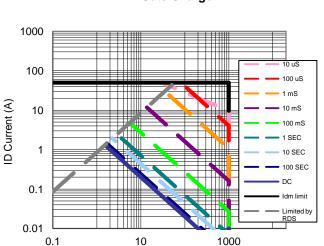


6. Capacitance

Typical Electrical Characteristics

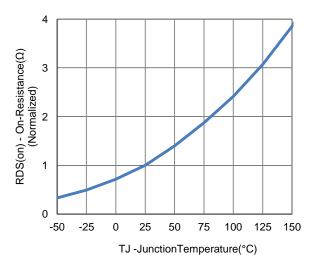


7. Gate Charge

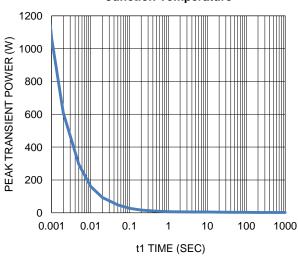


VDS Drain to Source Voltage (V)

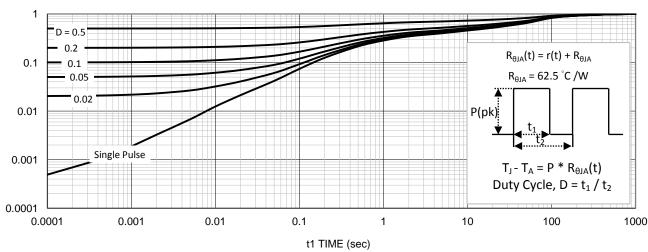
9. Safe Operating Area



8. Normalized On-Resistance Vs Junction Temperature

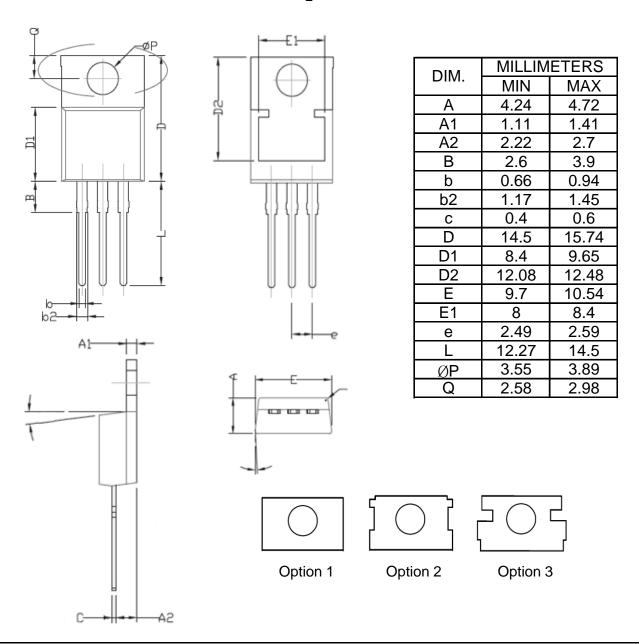


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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