

## N-Channel 40-V (D-S) MOSFET

### Key Features:

- Low  $r_{DS(on)}$  trench technology
- Low thermal impedance
- Fast switching speed

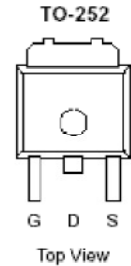
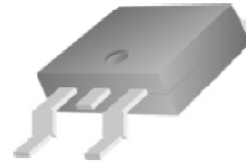
### Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ (m $\Omega$ )	$I_D$ (A)
40	2.3 @ $V_{GS} = 10V$	100
	4.5 @ $V_{GS} = 4.5V$	65



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**FREE**



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		$V_{DS}$	40	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>a</sup>	$T_A = 25^\circ\text{C}$	$I_D$	100	A
Pulsed Drain Current <sup>b</sup>		$I_{DM}$	300	
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	56	A
Power Dissipation <sup>a</sup>	$T_A = 25^\circ\text{C}$	$P_D$	50	W
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>a</sup>	$R_{\theta JA}$	40	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Case	$R_{\theta JC}$	3	

### Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

## Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static</b>						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 32 V, V_{GS} = 0 V$			1	$\mu A$
		$V_{DS} = 32 V, V_{GS} = 0 V, T_J = 55^\circ C$			25	
On-State Drain Current	$I_{D(on)}$	$V_{DS} = 5 V, V_{GS} = 10 V$	150			A
Drain-Source On-Resistance	$r_{DS(on)}$	$V_{GS} = 10 V, I_D = 20 A$			2.3	$m\Omega$
		$V_{GS} = 4.5 V, I_D = 16 A$			4.5	
Forward Transconductance	$g_{fs}$	$V_{DS} = 15 V, I_D = 20 A$		30		S
Diode Forward Voltage	$V_{SD}$	$I_S = 28 A, V_{GS} = 0 V$		0.83		V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 20 V, V_{GS} = 4.5 V,$ $I_D = 20 A$		60		nC
Gate-Source Charge	$Q_{gs}$			18		
Gate-Drain Charge	$Q_{gd}$			19		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 20 V, R_L = 1 \Omega,$ $I_D = 20 A,$ $V_{GEN} = 10 V, R_{GEN} = 6 \Omega$		21		ns
Rise Time	$t_r$			22		
Turn-Off Delay Time	$t_{d(off)}$			160		
Fall Time	$t_f$			50		
Input Capacitance	$C_{iss}$	$V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz$		5000		pF
Output Capacitance	$C_{oss}$			600		
Reverse Transfer Capacitance	$C_{rss}$			400		

## Notes

- Pulse test: PW  $\leq$  300us duty cycle  $\leq$  2%.
- Guaranteed by design, not subject to production testing.

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